

April 20 '07

RP-note4 Block Diagram

06239-1-0420

Project Code: 91.4T401.001

PCB(Raw Card):

PCB Layer Stackup

- L1:Component
- L2:GND
- L3:Signal 1
- L4:Signal 2
- L5:VCC
- L6:Signal 3
- L7:GND
- L8:Component

System DC/DC

MAX17003	81
INPUTS	OUTPUTS
VINT20	VCC5M
	VCC3M

Battery Charger/Selector

MAX8765	76
DOCK_PWR20_F	M-BAT-PWR
	S-BAT-PWR

VCC1R8A

MAX8632	86
VINT20	VCC1R8A
	VCC0R9B

CPU DC/DC

ADP3207A	82, 83
VINT20	VCCCPUCORE

VCC1R05B/VCC1R5M

MAX1540	87
VINT20	VCC1R5M
	VCC1R05B

VCCGFXCORE

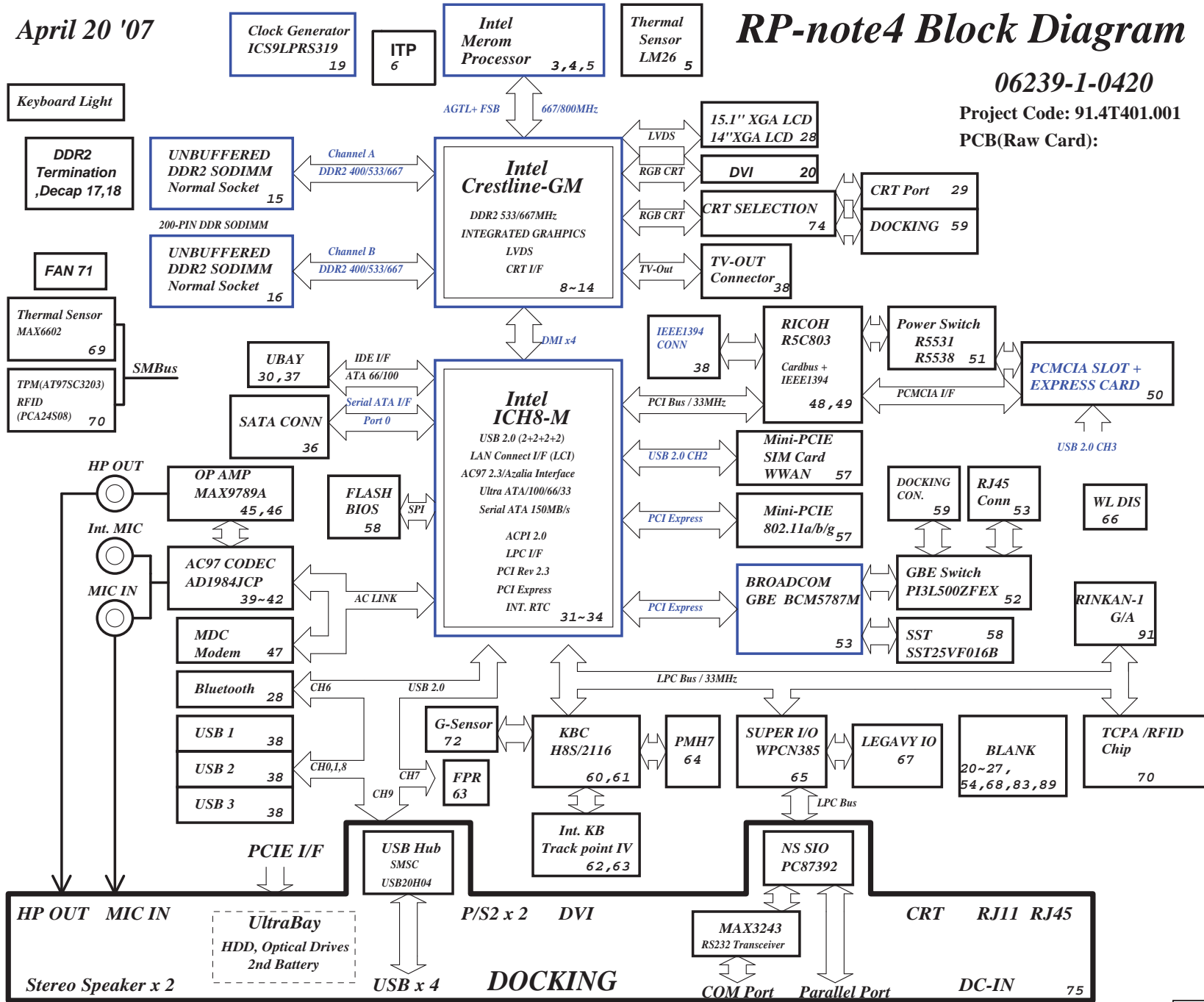
ADP3209	85
VINT20	VCCGFXCORE

VCC1R25B

APL5912 88

VCC1R2M

92
VCC2R5M 93



緯創資通

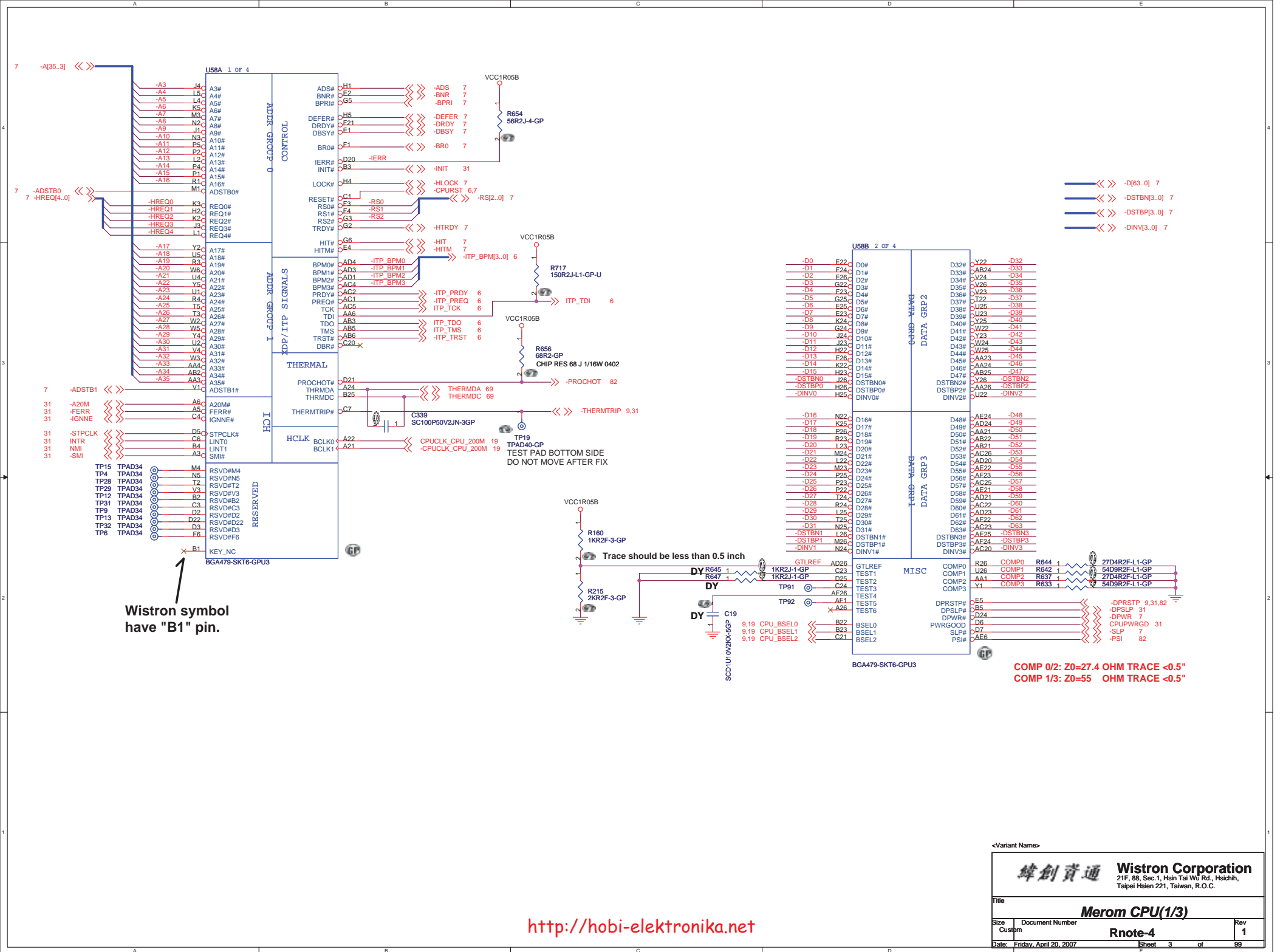
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

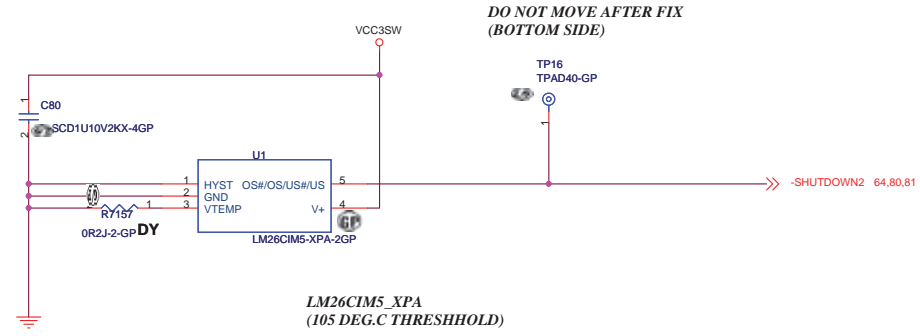
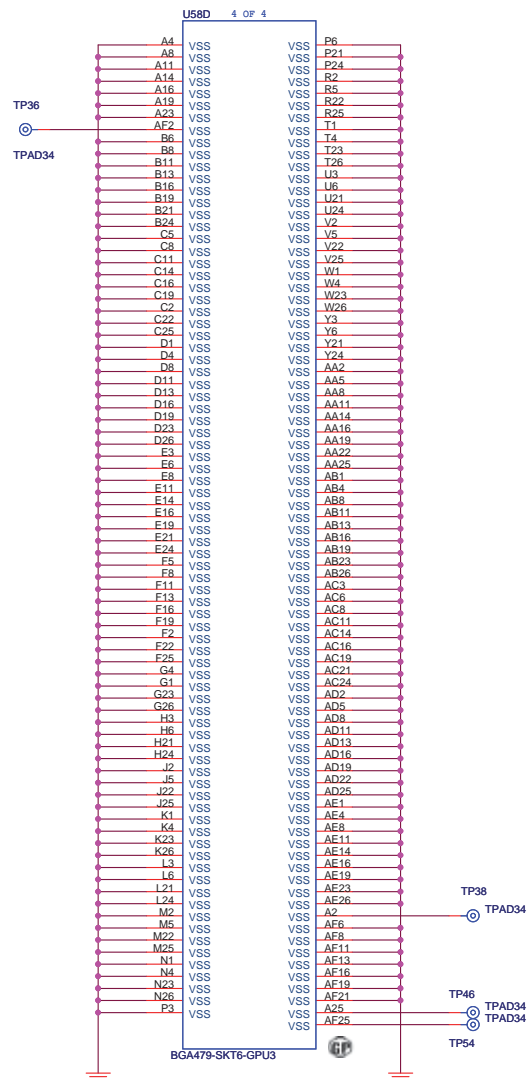
Block Diagram			
Size	Document Number	Rev	
Custom	Rnote-4	1	
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Wistron symbol
have "B1" pin.

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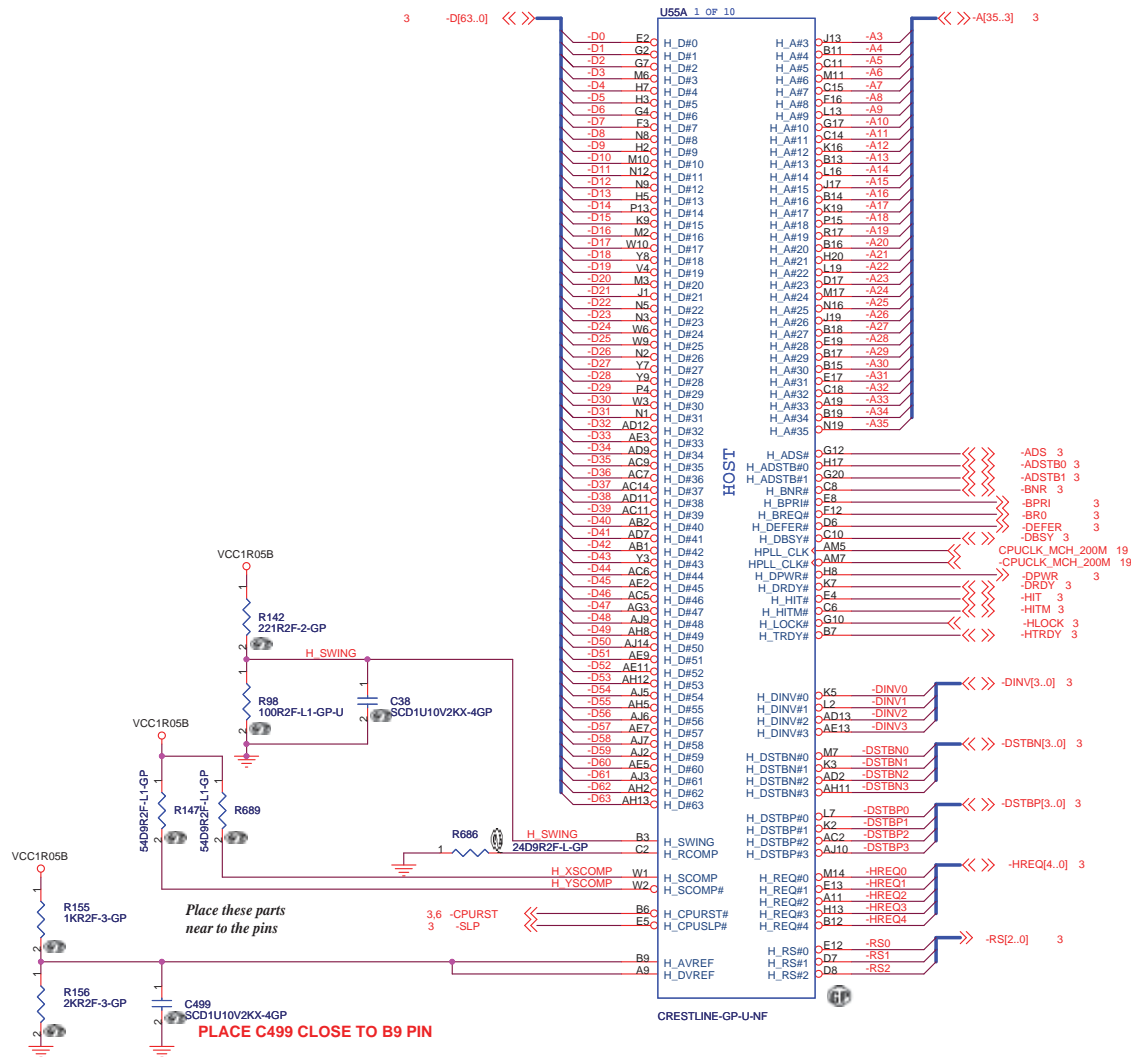


ADD Table	
U1	R7157
LM26 (NS)	NOASM
MAX6519(MAXIM)	
MAX6501(MAXIM)	ASM
ADT6501(ADI)	

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NOTE: C445,C476 AND R477 "ASM" BEFORE SIT,"NO_ASM" FROM SIT.



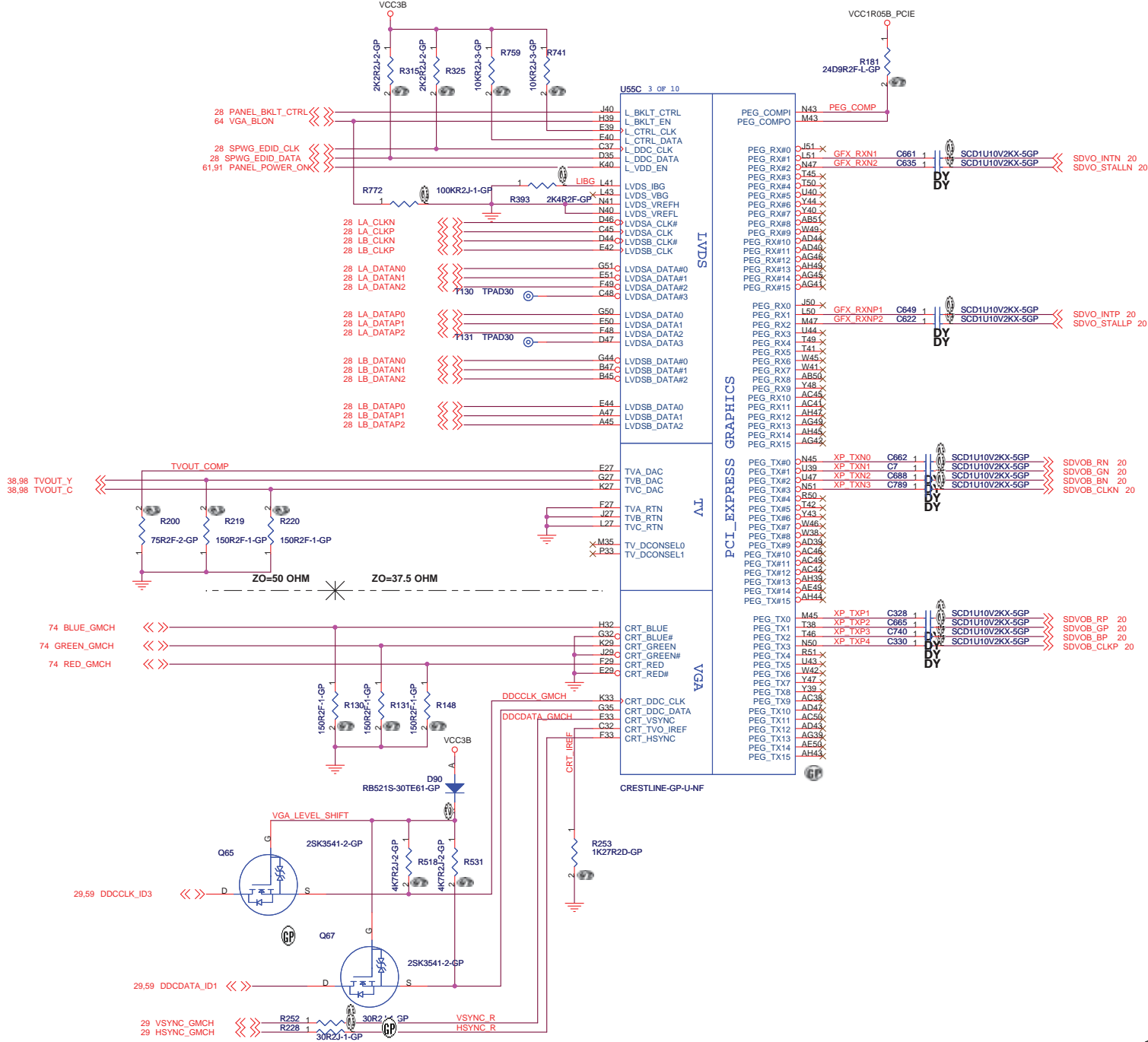
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Crestline(1/7):HOST I/F	
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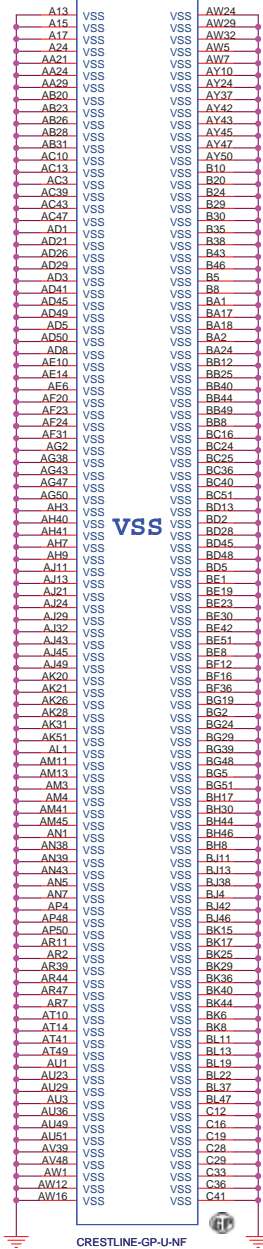
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Size	Document Number	Rev	
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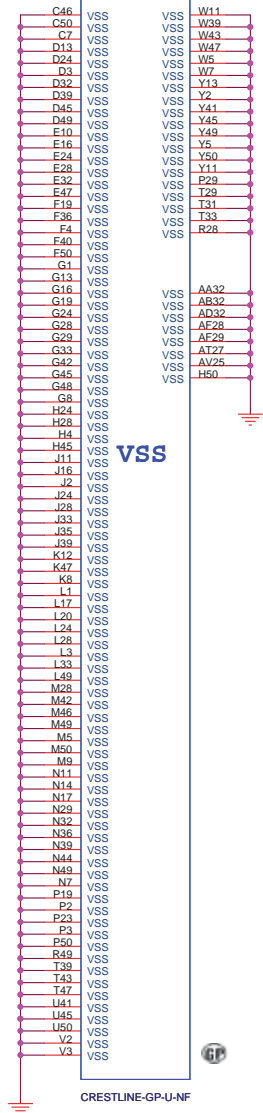
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U55J0 0 OF 10



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Rev 1	

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Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipet Hsien 221, Taiwan, R.O.C.

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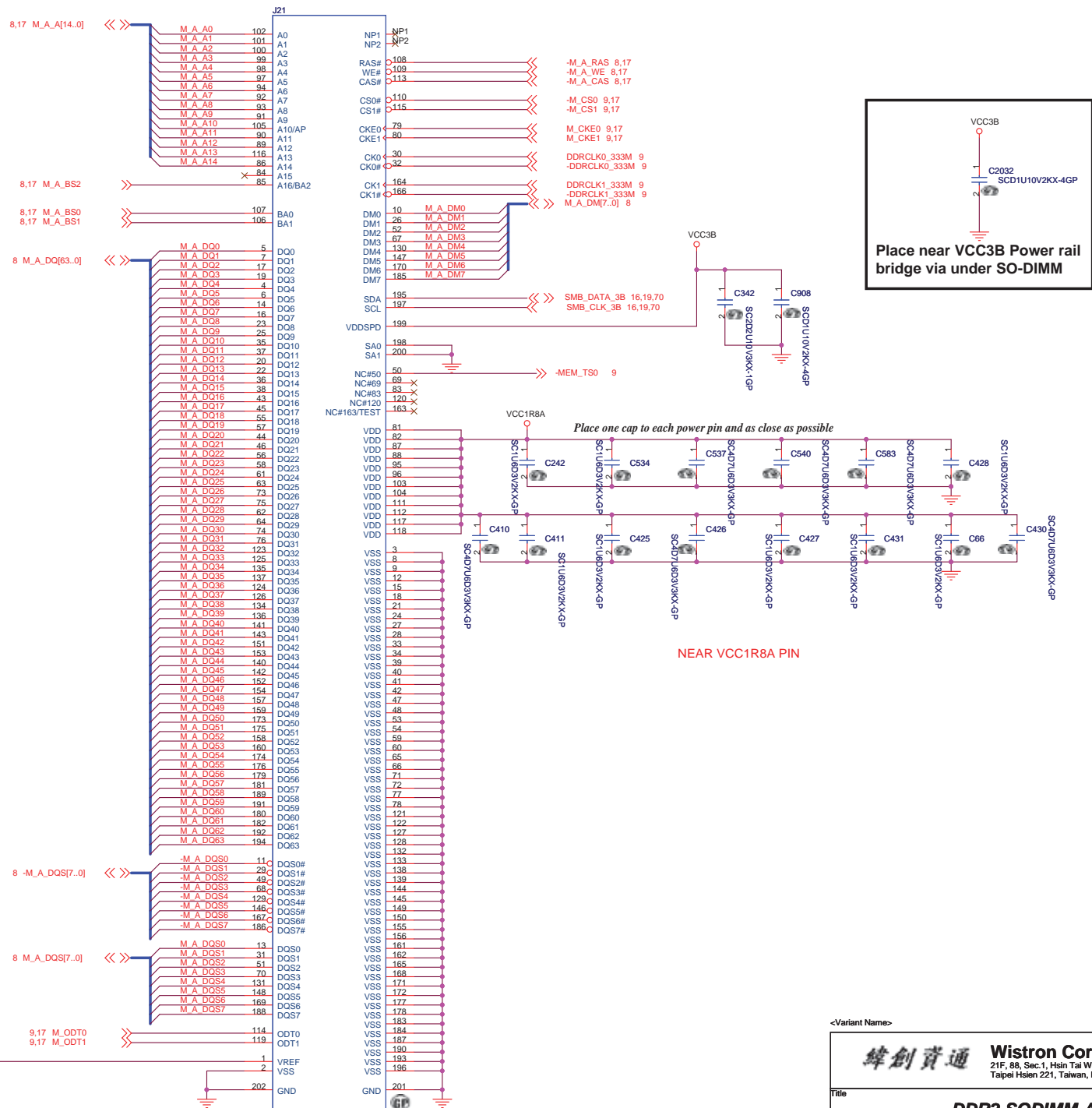
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Rnote-4

Rev
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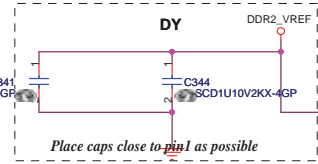
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Place near VCC3B Power rail bridge via under SO-DIMM

NEAR VCC1R8A PIN



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<Variant Name>

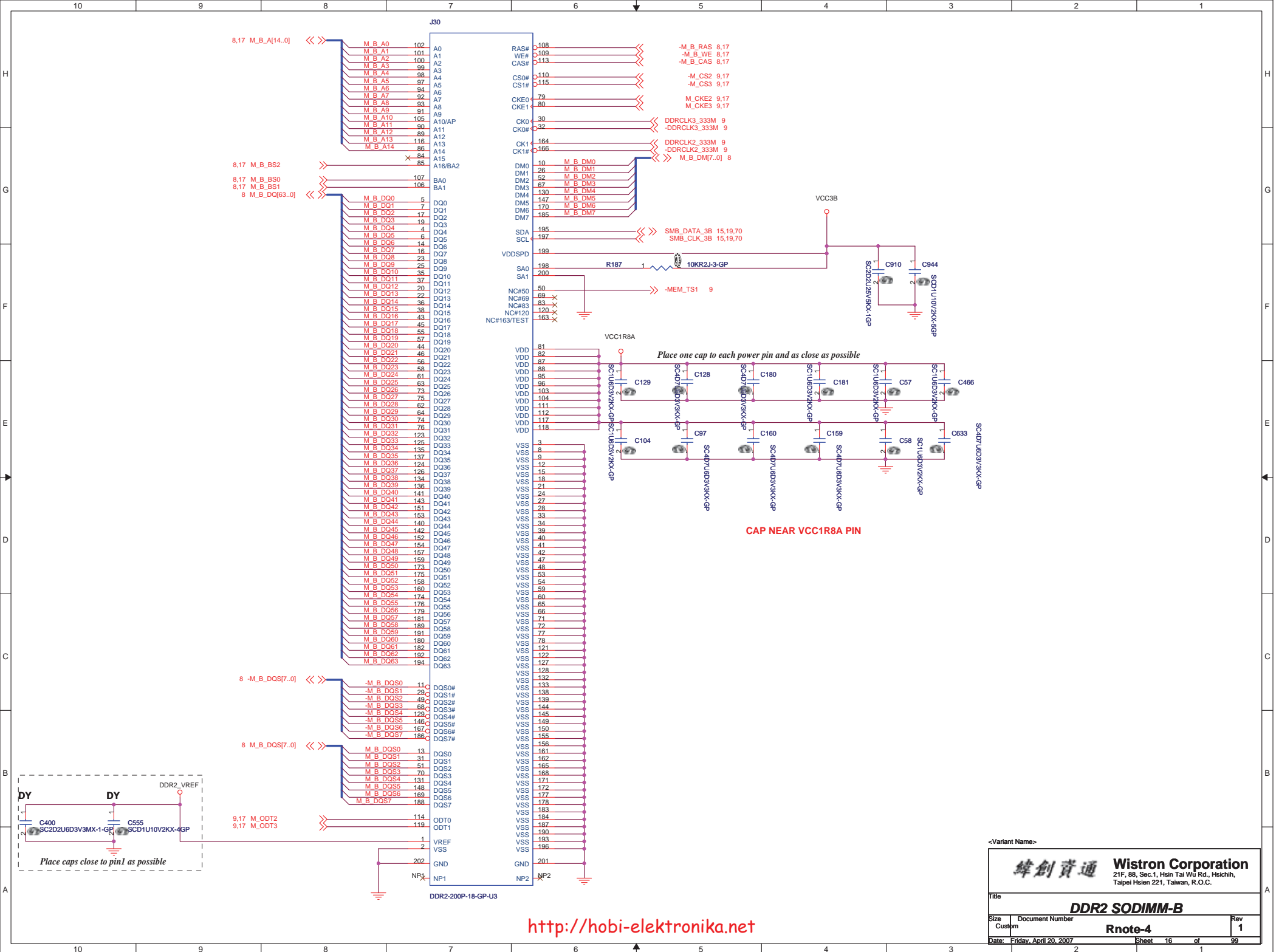
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

DDR2 SODIMM-A

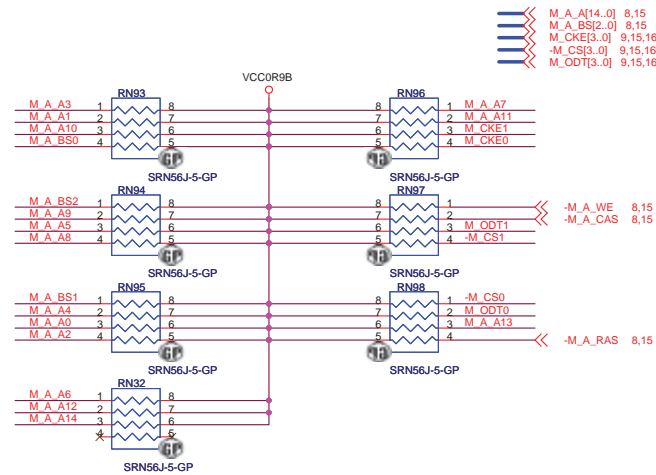
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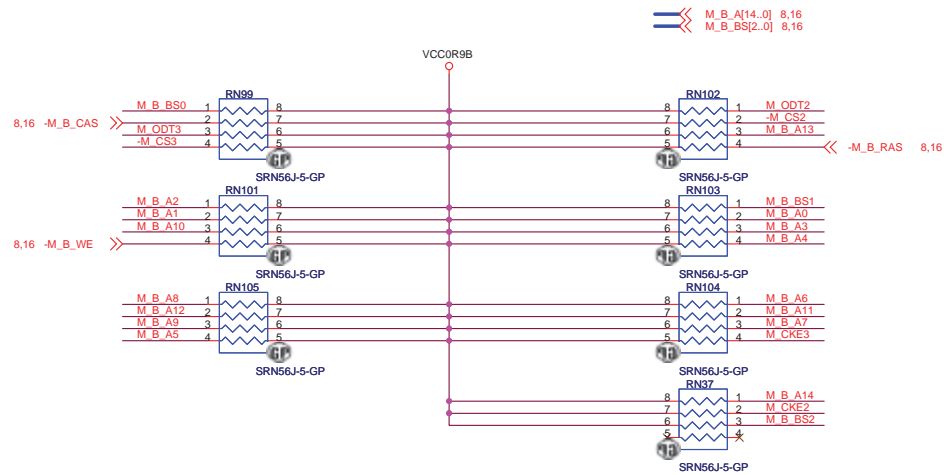
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CHANNEL A PARALLEL TERMINATION



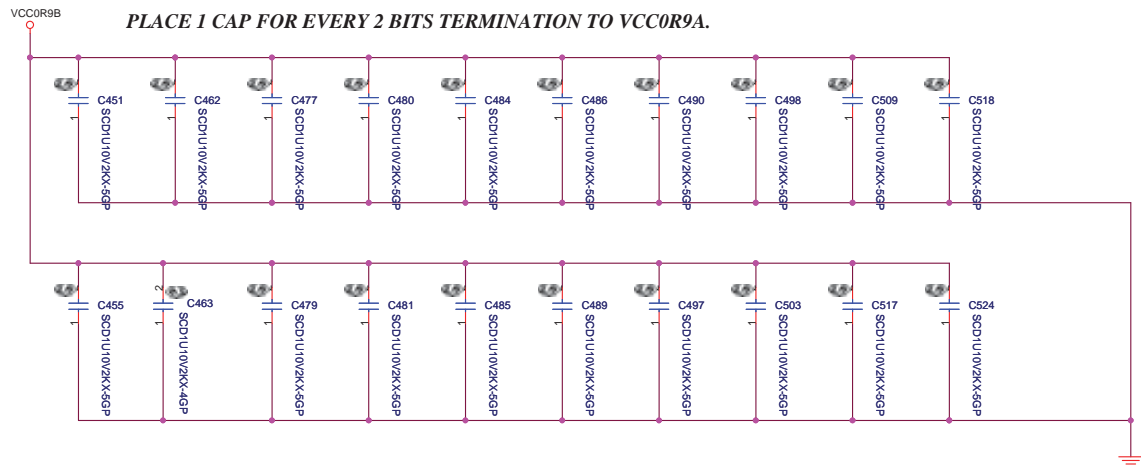
CHANNEL B PARALLEL TERMINATION




<http://hobi-elektronika.net>

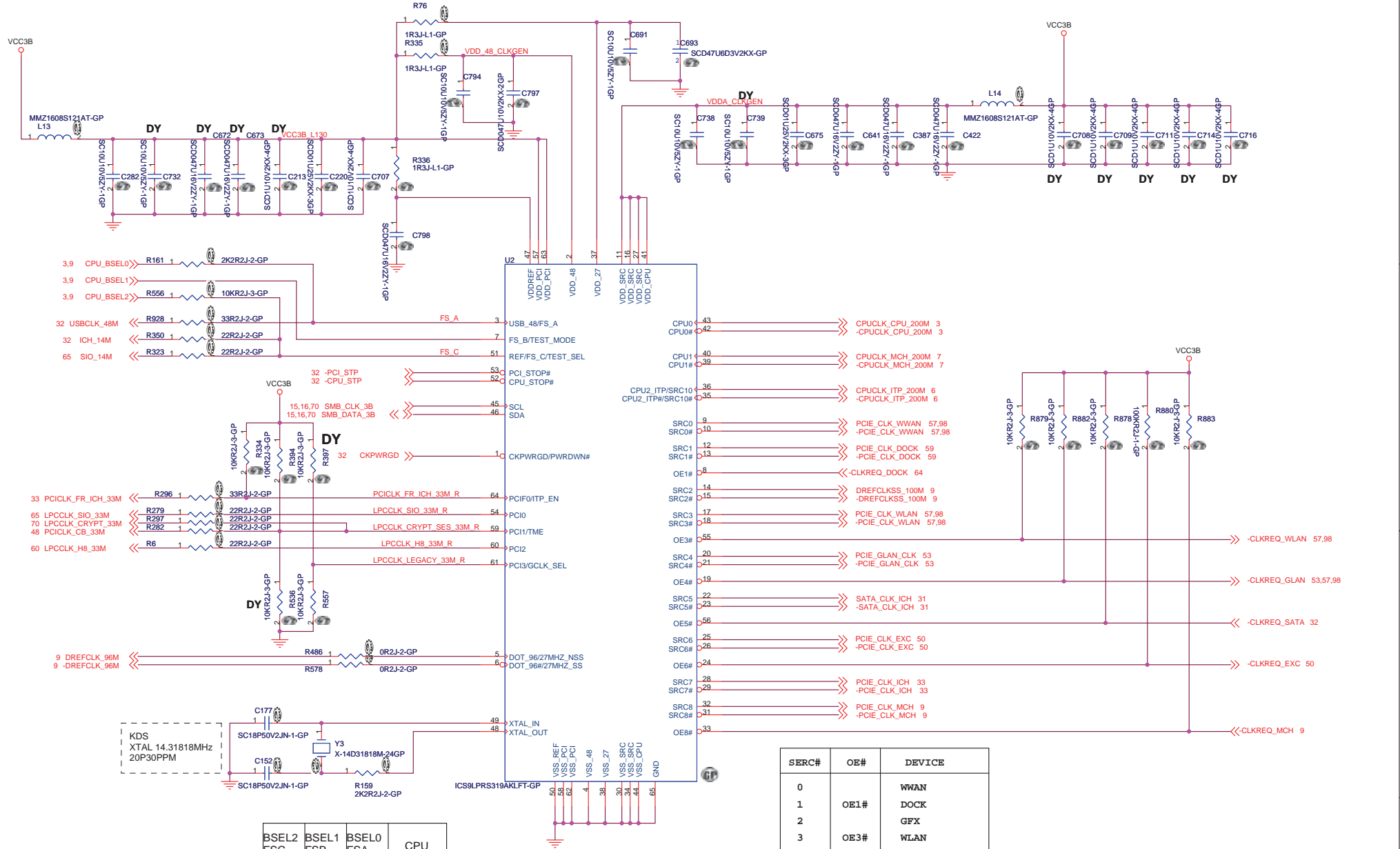
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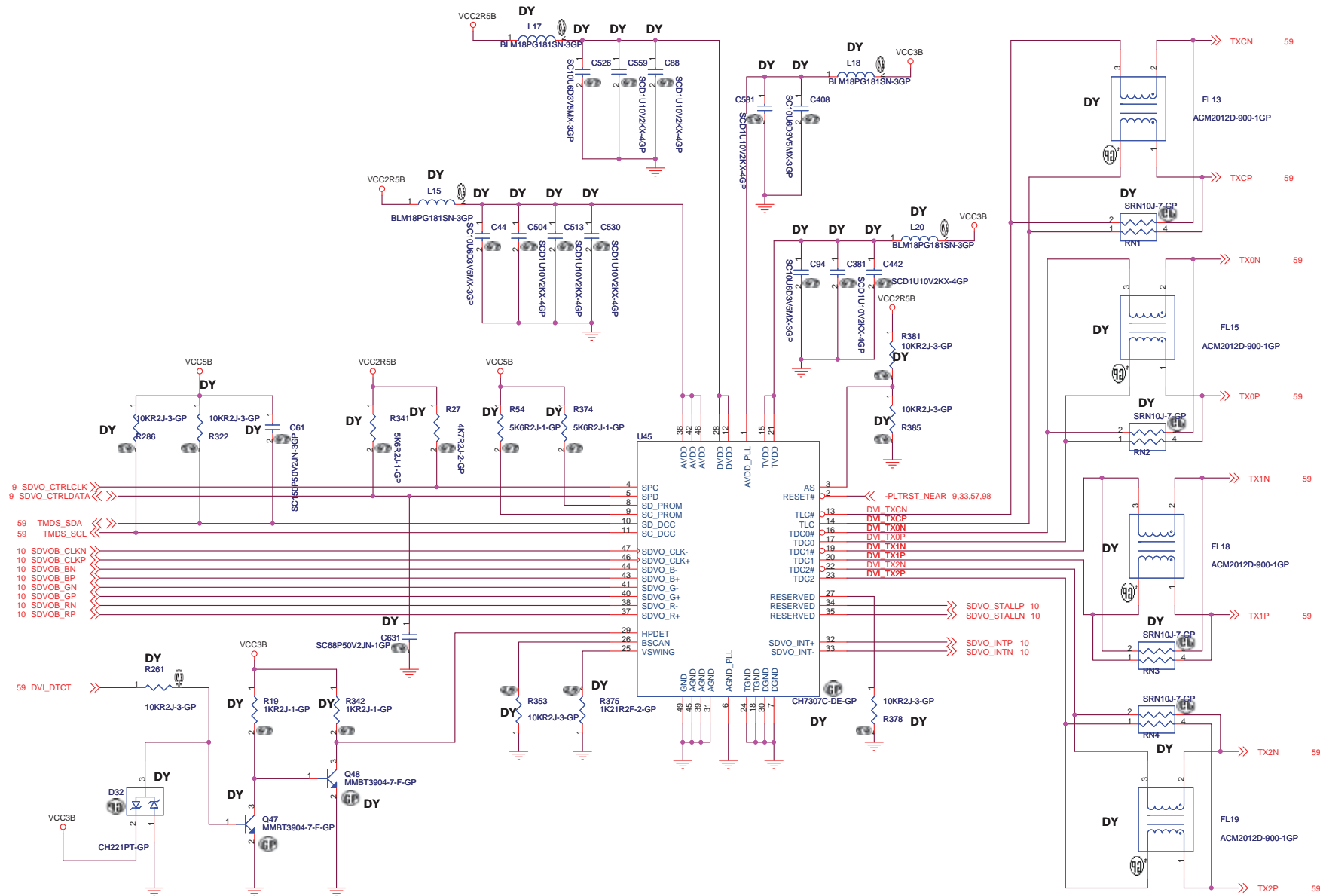
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Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

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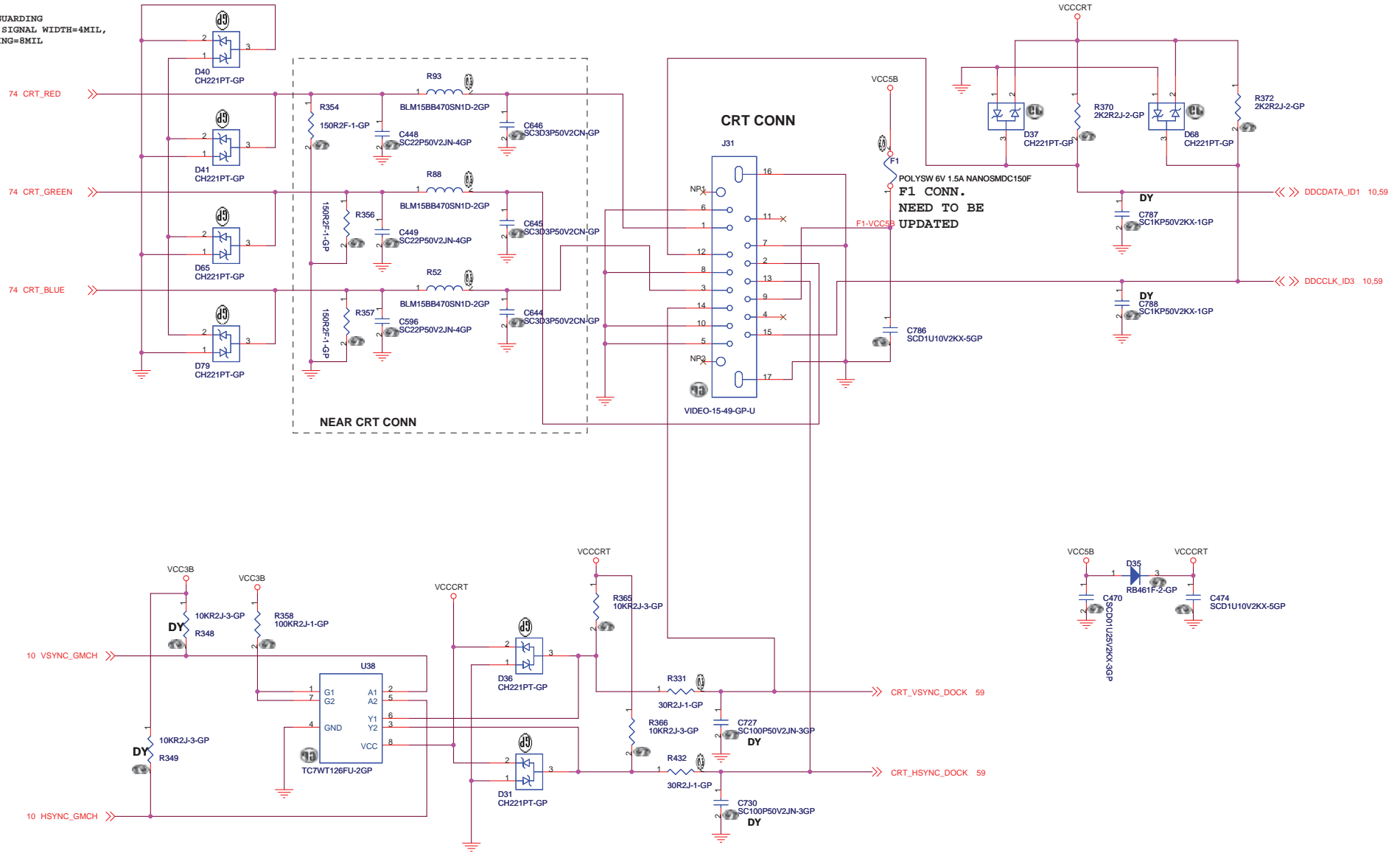
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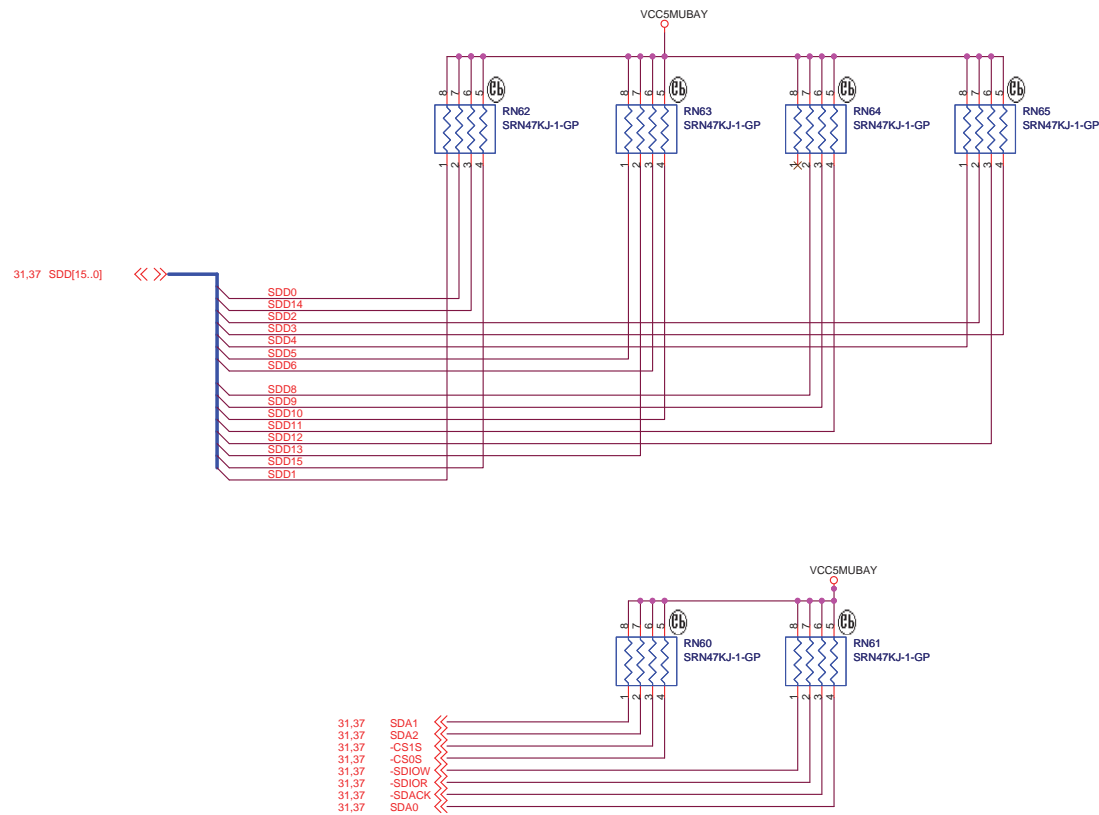


GND GUARDING
EACH SIGNAL WIDTH=4MIL,
SPACING=8MIL



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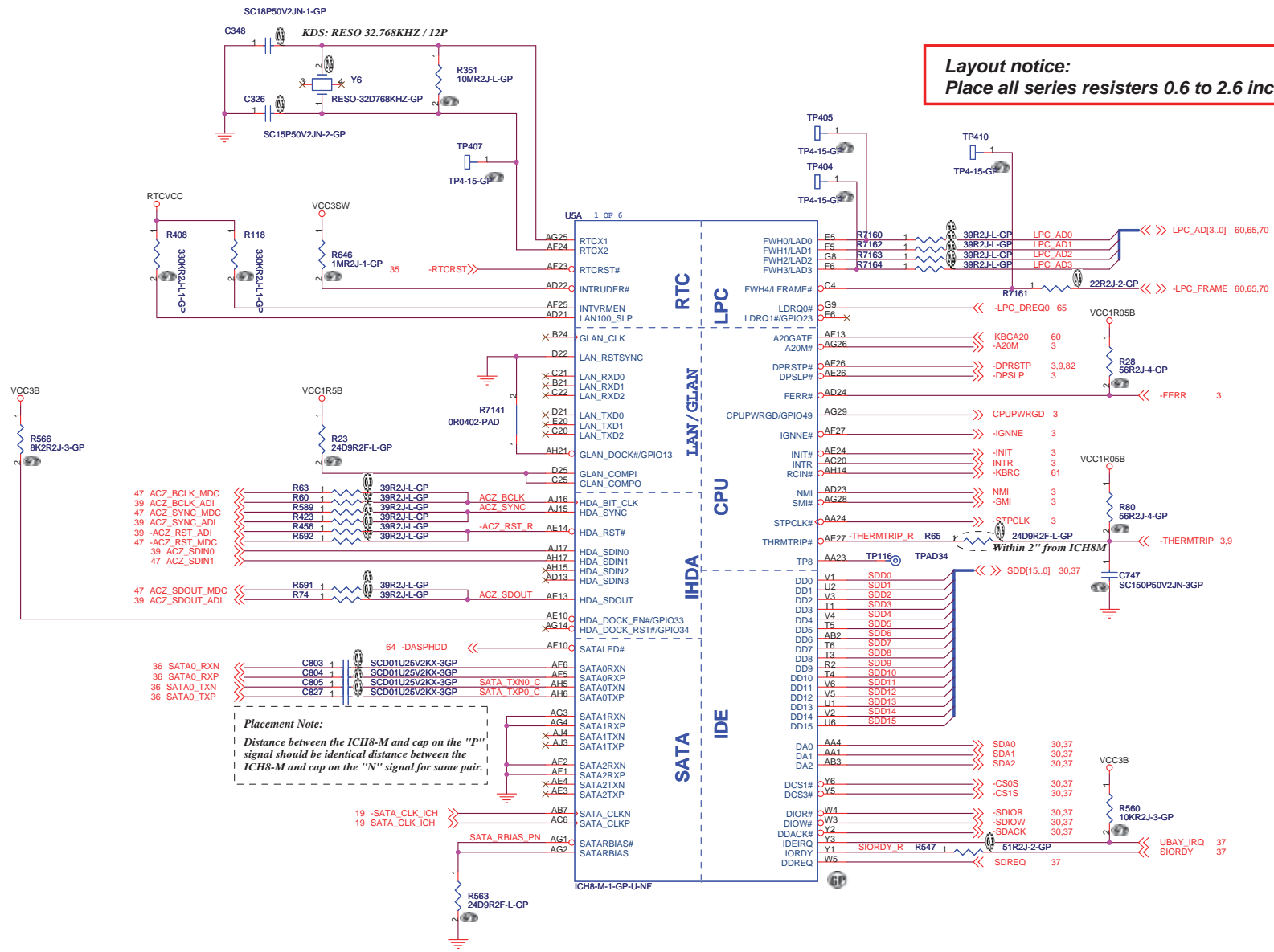
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Title			
PATA I/F PU			
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Layout notice:
Place all series resistors 0.6 to 2.6 inch from the ICH8



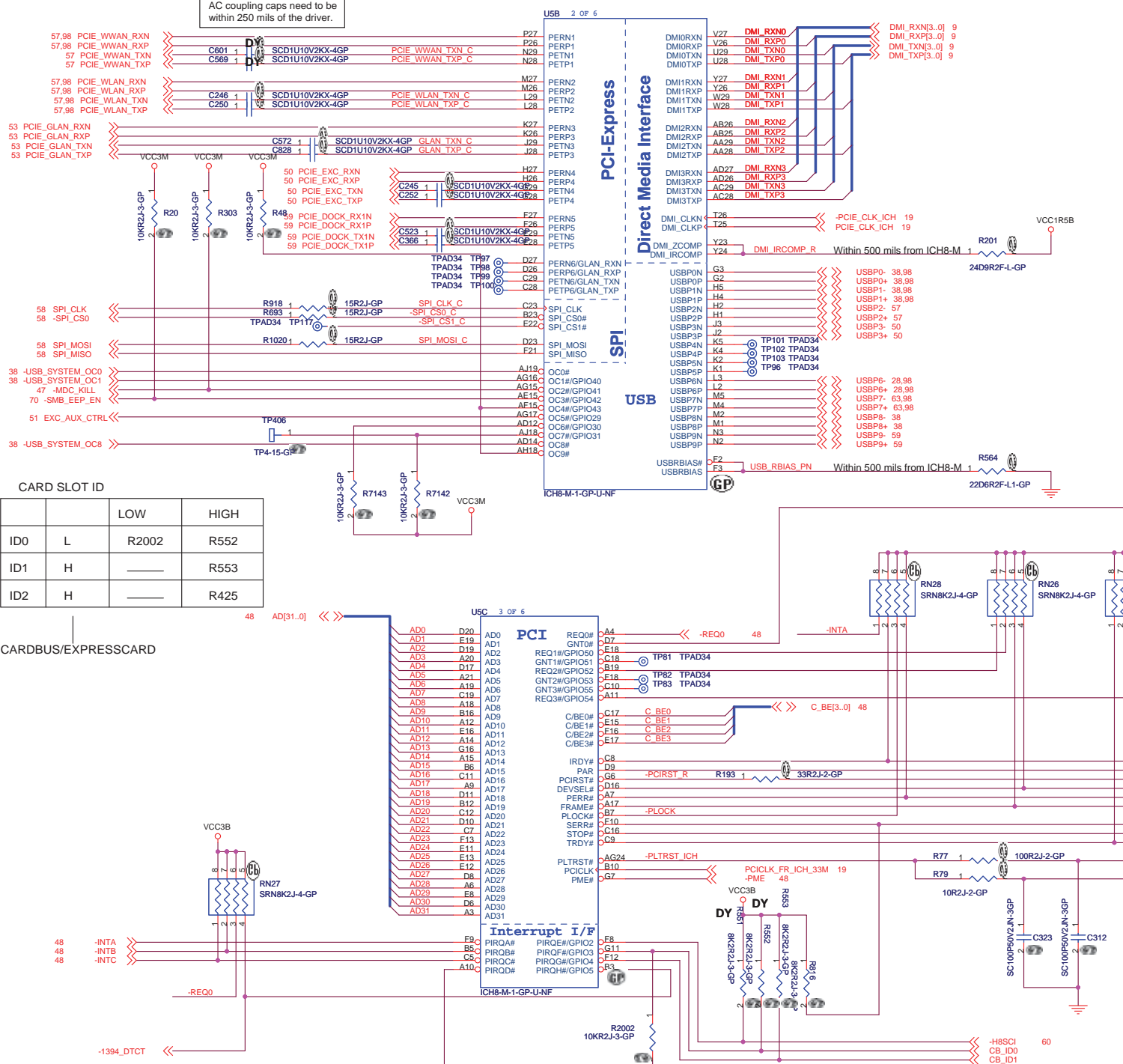
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4IN1 SLOT	R280	R561
YES	NO_ASM	ASM
NO	ASM	NO_ASM

- LOGIC

AC coupling caps need to be within 250 mils of the driver.



- USB table**
- 0 : USB Port 1
 - 1 : USB Port 2
 - 2 : Mini PCIe slot
 - 3 : EXC Card slot
 - 4 : Reserved
 - 5 : Reserved
 - 6 : Bluetooth
 - 7 : FingerPrint
 - 8 : USB Port 3
 - 9 : DOCKING USB

CARD SLOT ID

		LOW	HIGH
ID0	L	R2002	R552
ID1	H	---	R553
ID2	H	---	R425

CARDBUS/EXPRESSCARD

	BIOS
LOW	SPI
HIGH	LPC(FWH)

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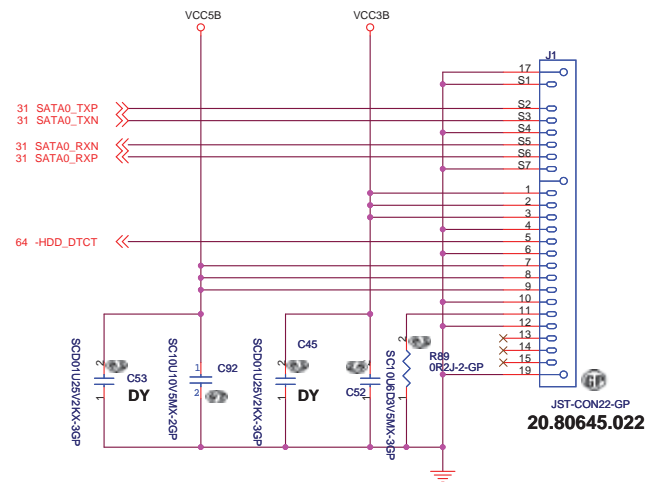
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 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

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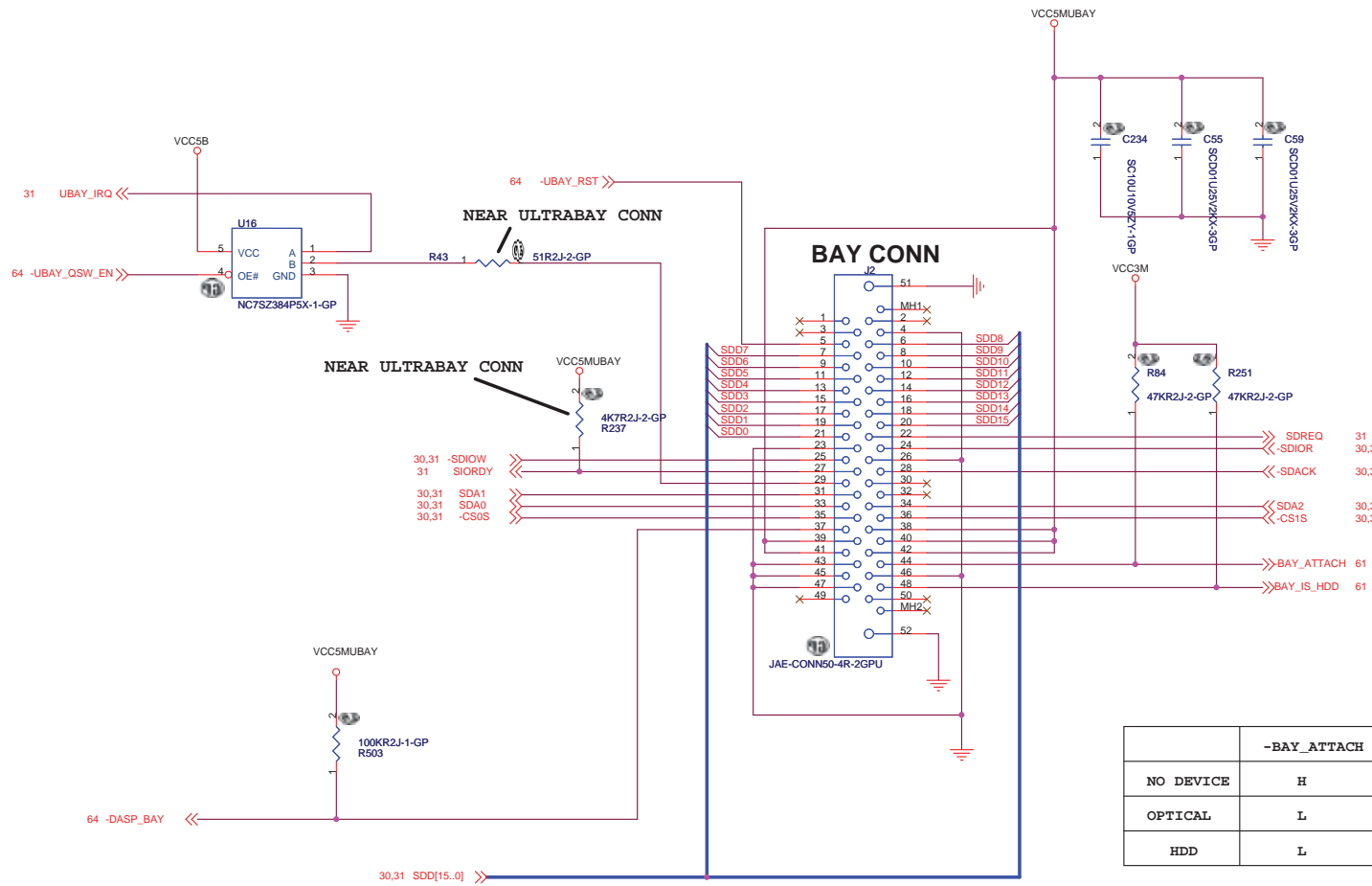


SATA



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SATA HDD CONN	
Size	Document Number
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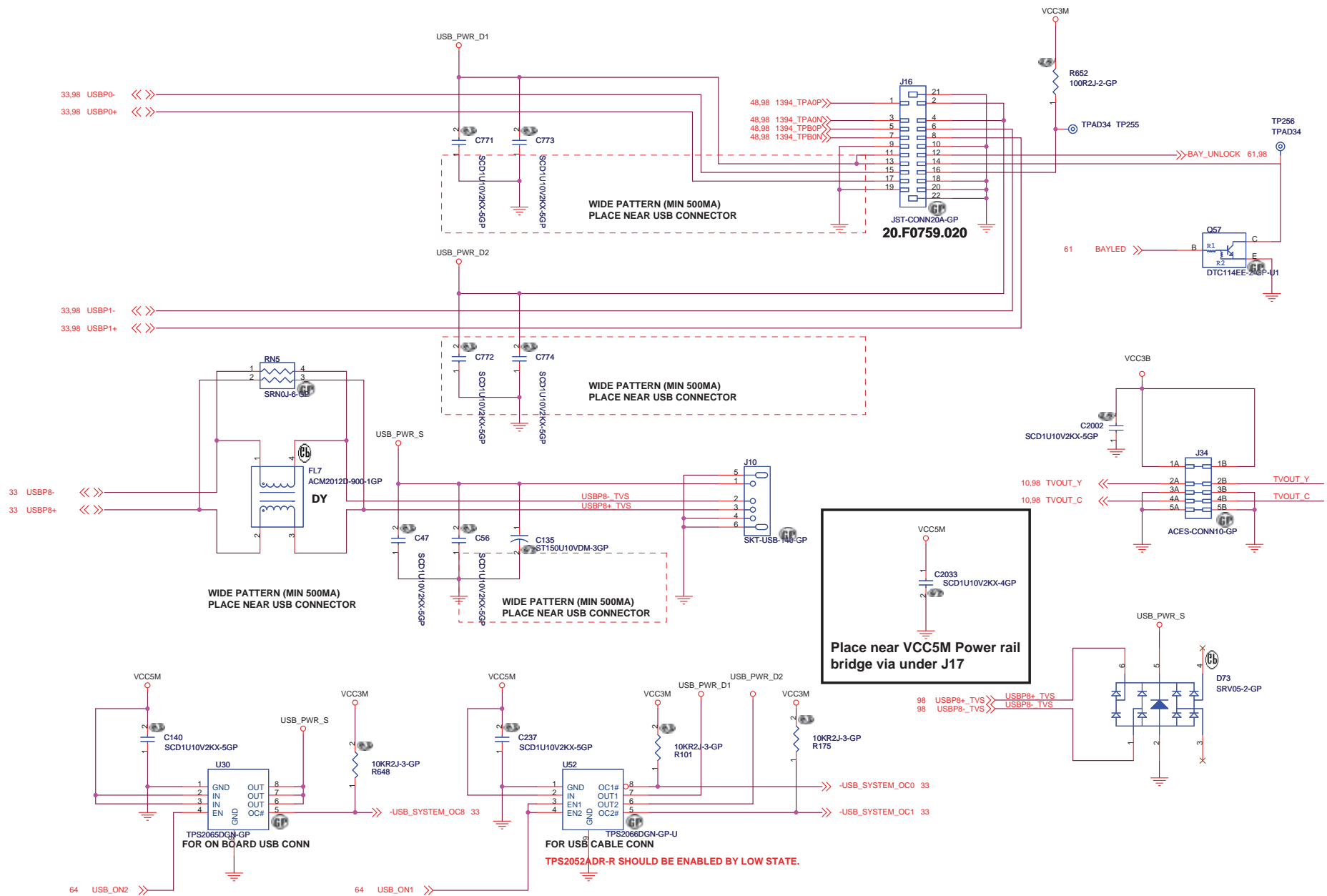


	-BAY_ATTACH	BAY_IS_HDD
NO DEVICE	H	DON'T CARE
OPTICAL	L	L
HDD	L	H

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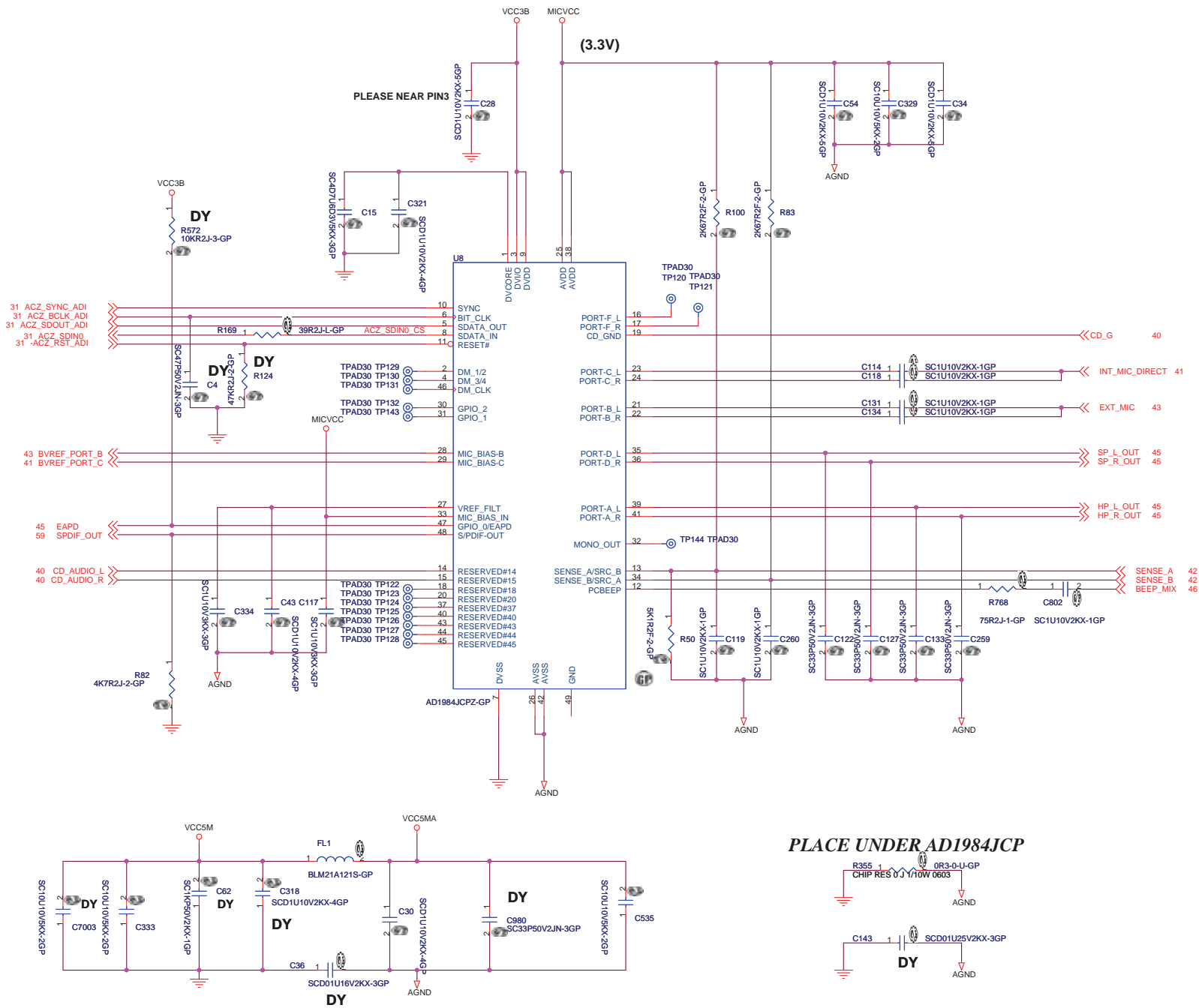
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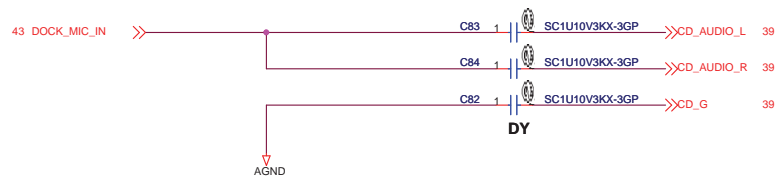
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USB POWER / CONNECTOR			
Size	Document Number	Rev	
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AUDIO AD1984JCP			
Size	Document Number	Rev	
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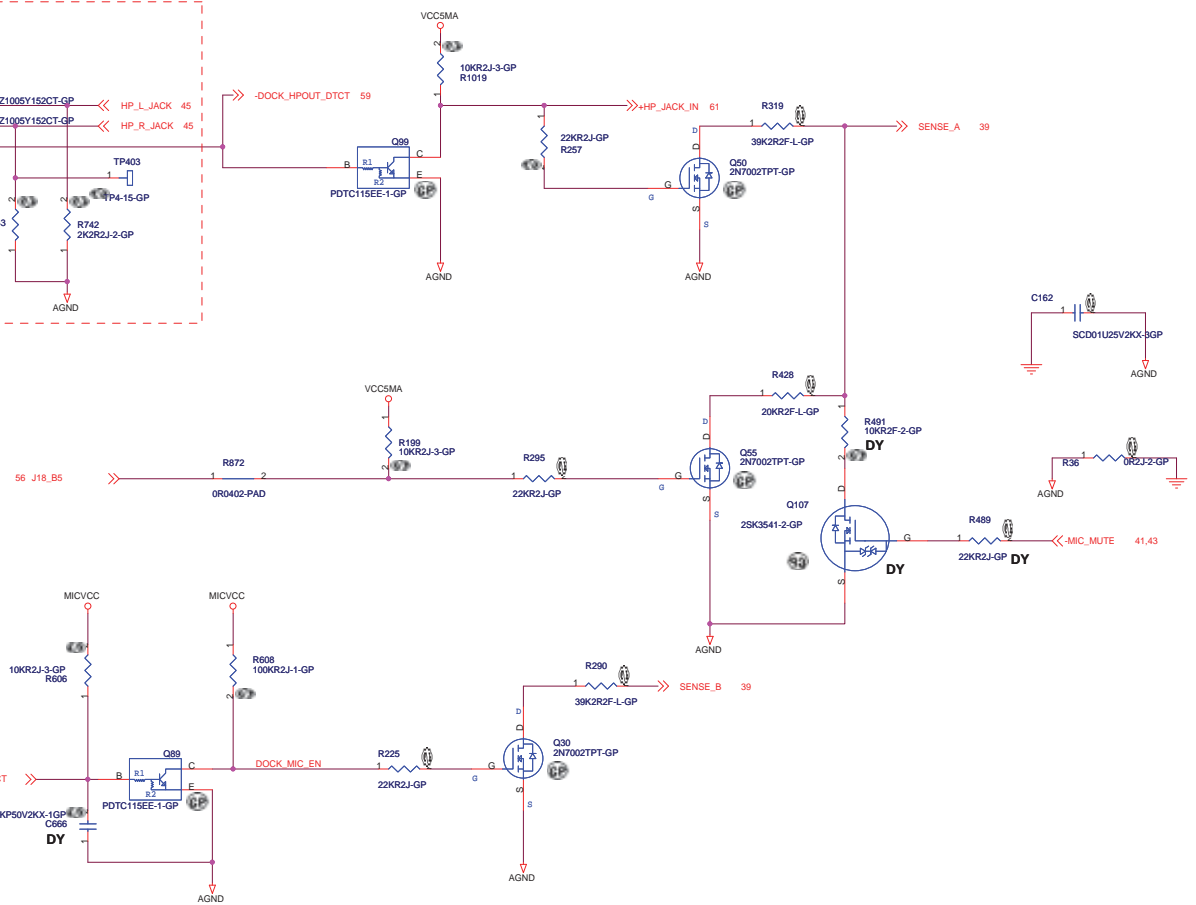
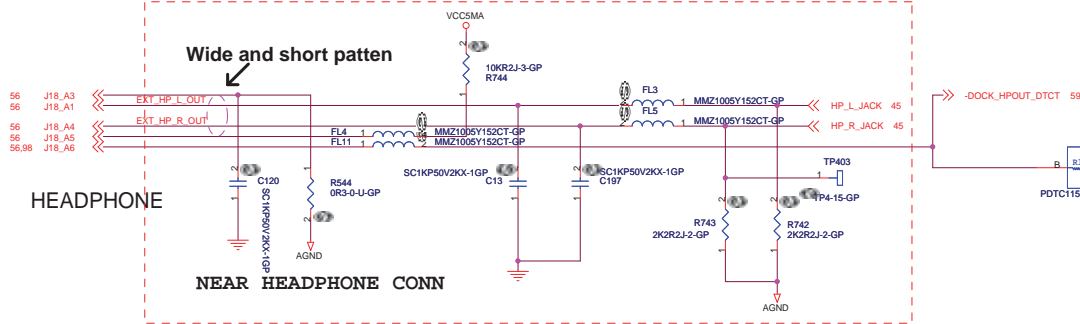


CONTACT NEAR DOCKING CONN

<http://hobi-elektronika.net>

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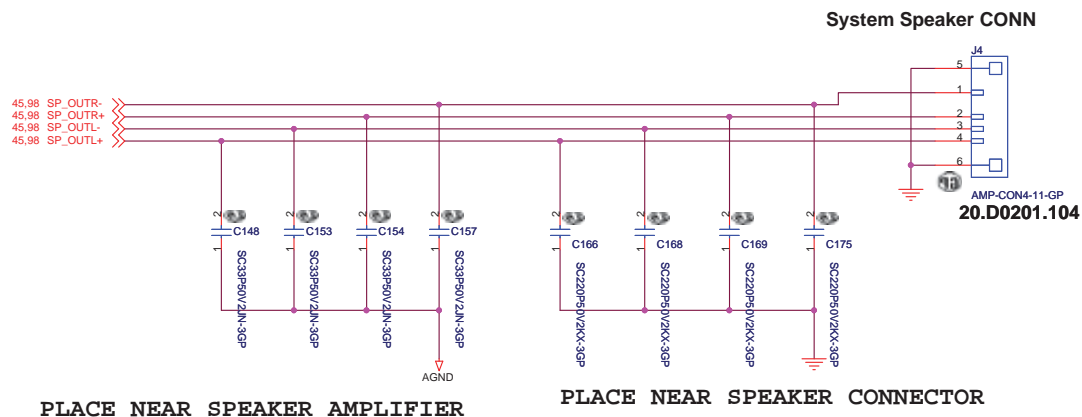
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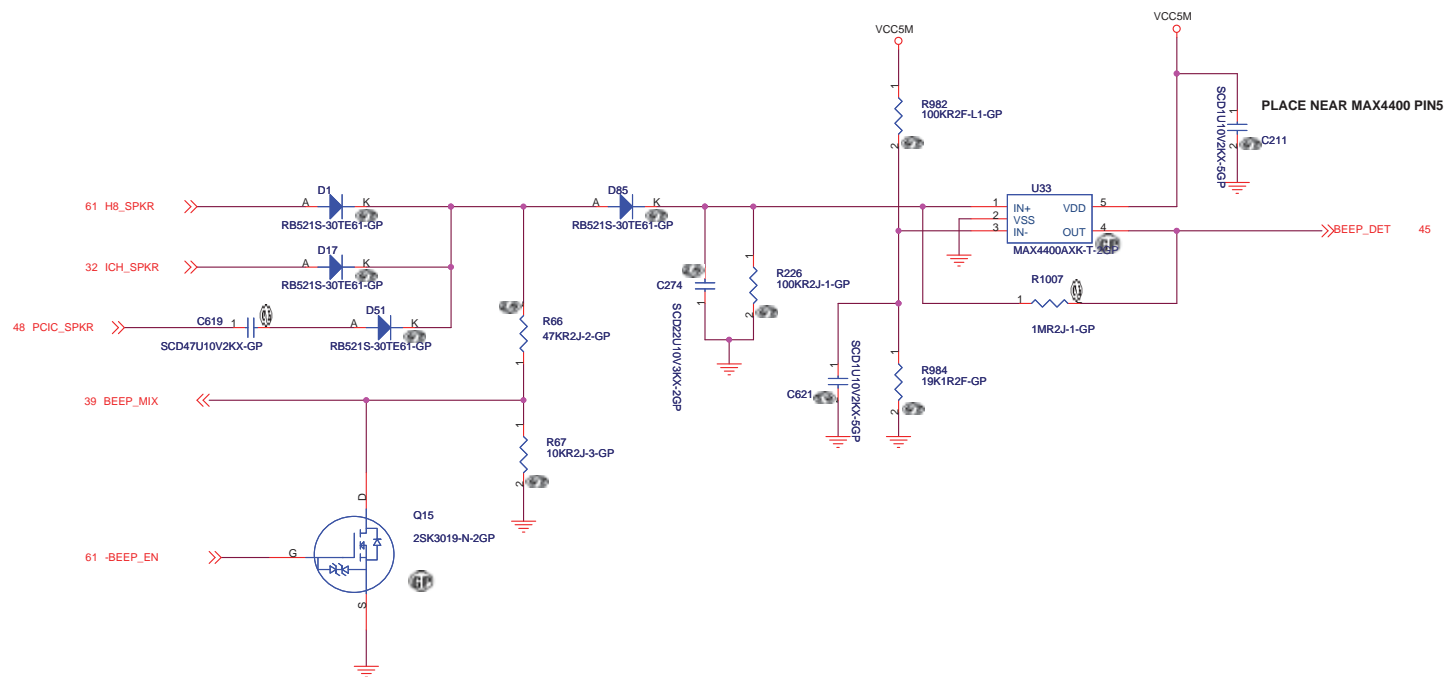
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AUDIO SYSTEM SPEAKER			
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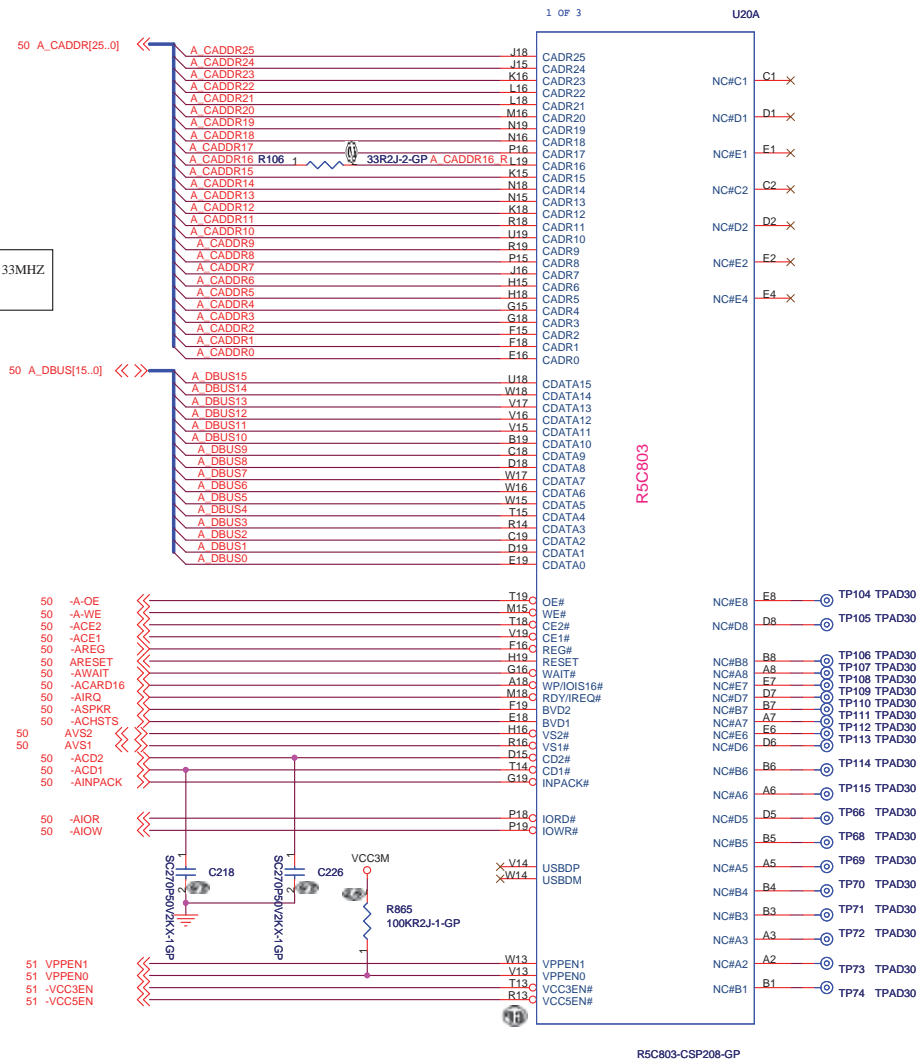


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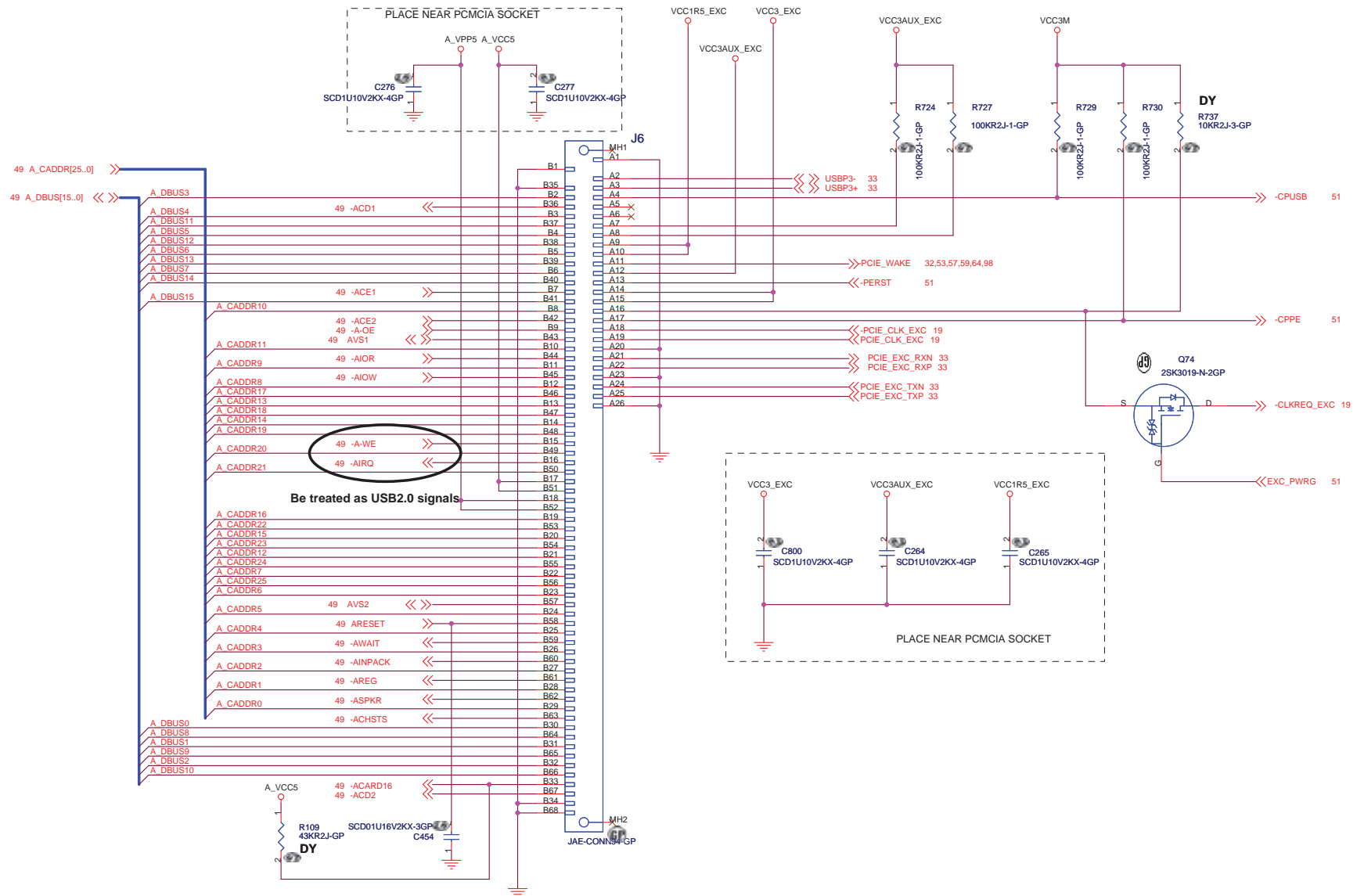
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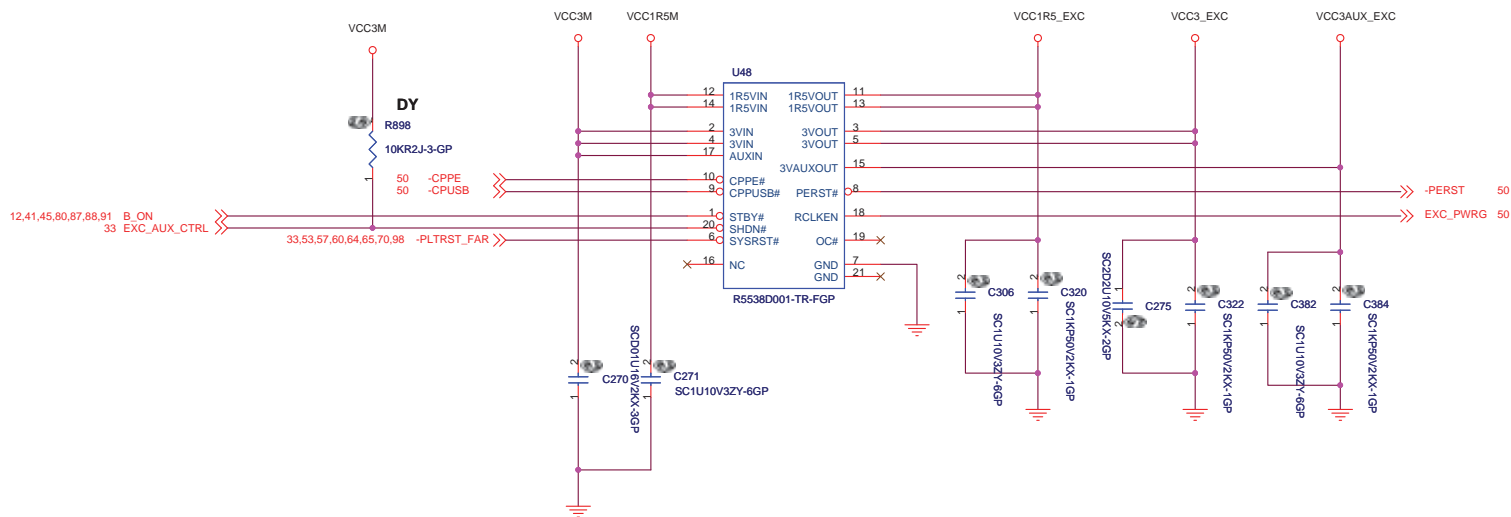
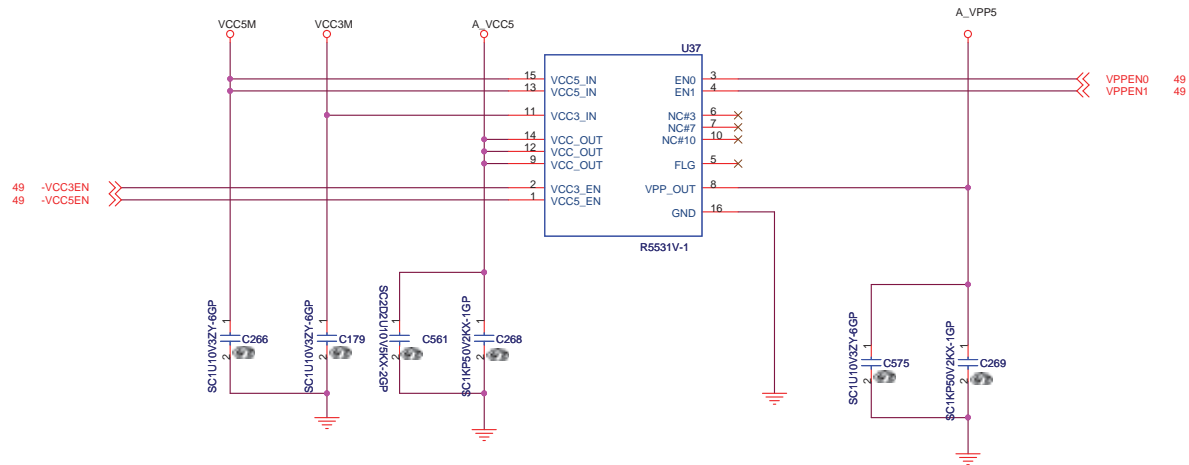
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Title		AUDIO HDP AMP MISC	
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A_CADDR16: CARDBUS CLK 33MHZ
-AIOR, -AIOV: USB2.0



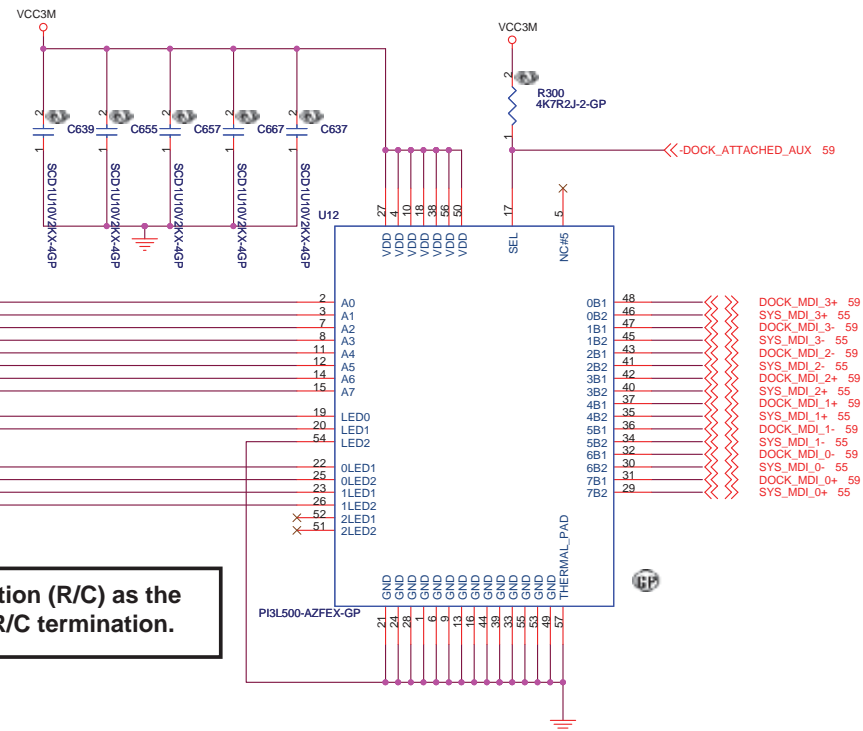
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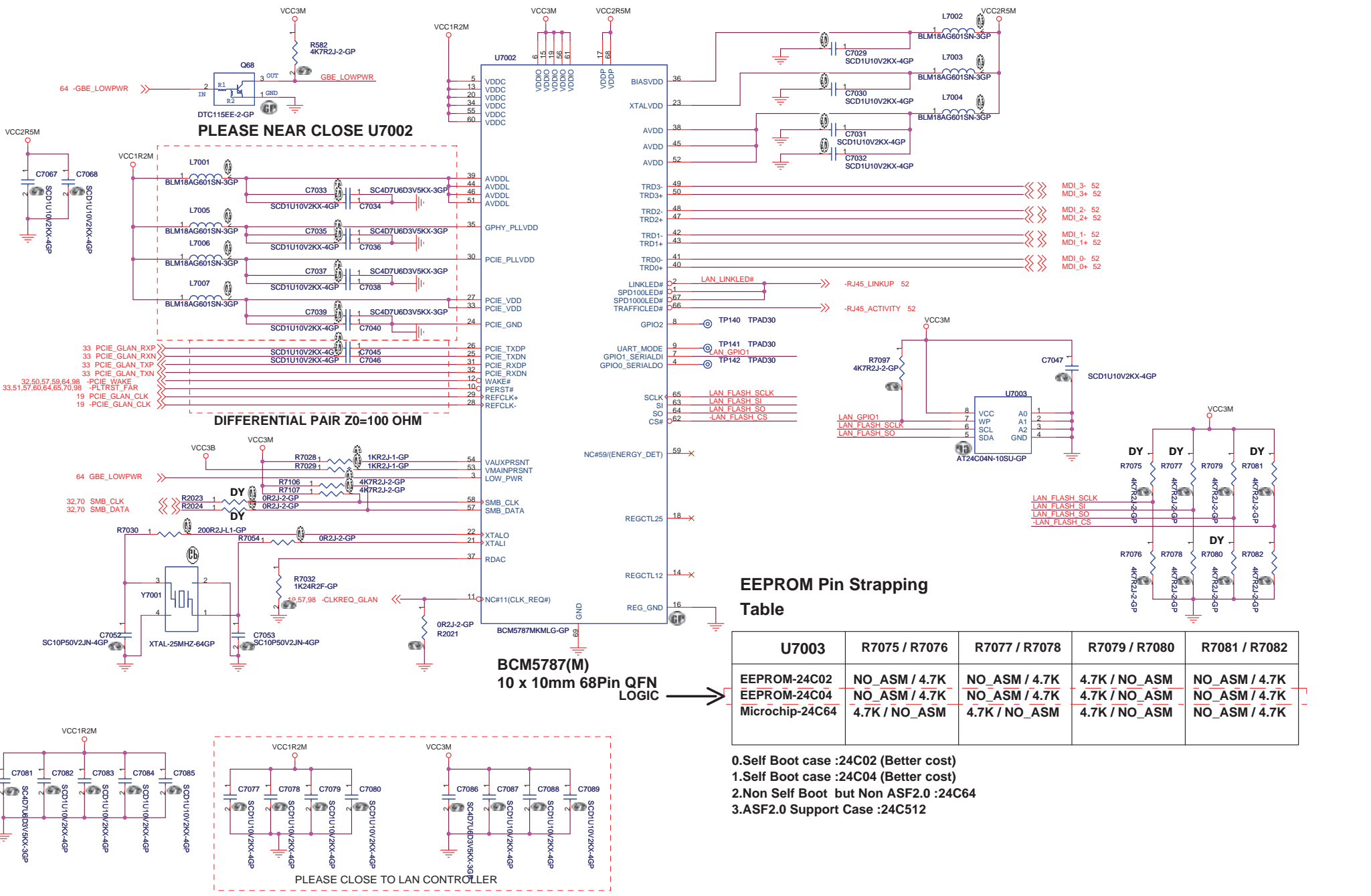
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53 MDI_3-
53 MDI_2+
53 MDI_2-
53 MDI_1+
53 MDI_1-
53 MDI_0+
53 MDI_0-
53 -RJ45_ACTIVITY
53 -RJ45_LINKUP
59 -RJ45_ACTIVITY_DOCK
56,98 -RJ45_ACTIVITY_SYS
59 -RJ45_LINKUP_DOCK
56,98 -RJ45_LINKUP_SYS

Do not install termination (R/C) as the 5787(M) has on-chip R/C termination.




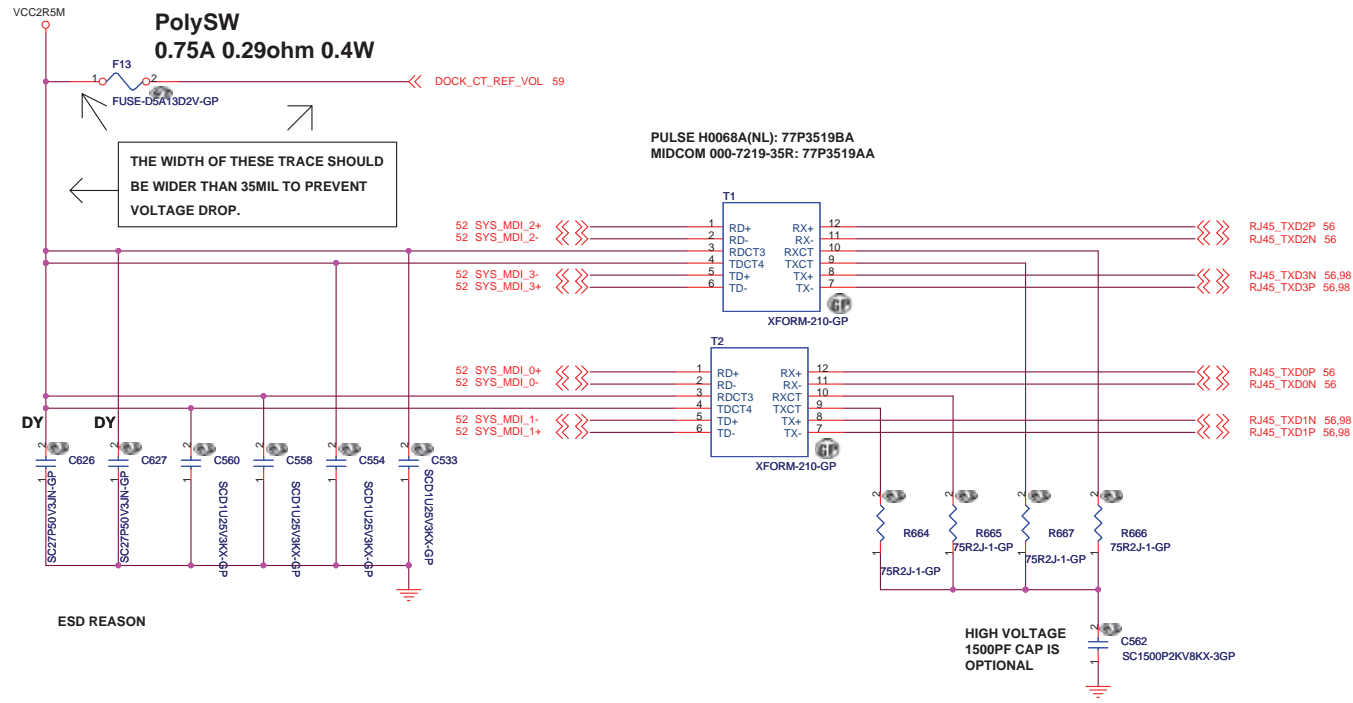
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Size	Document Number
A3	Rnote-4
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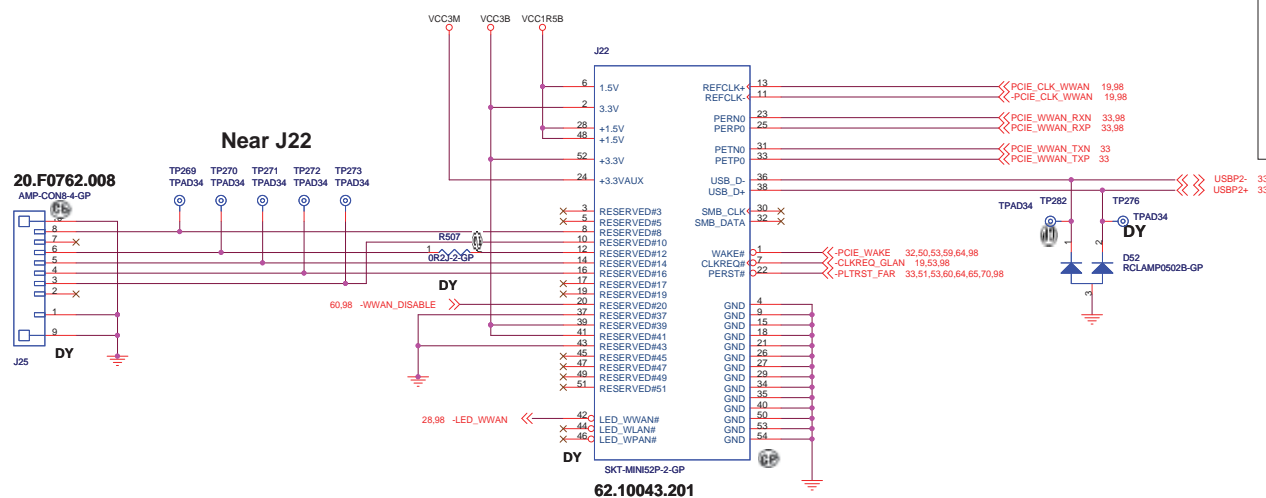


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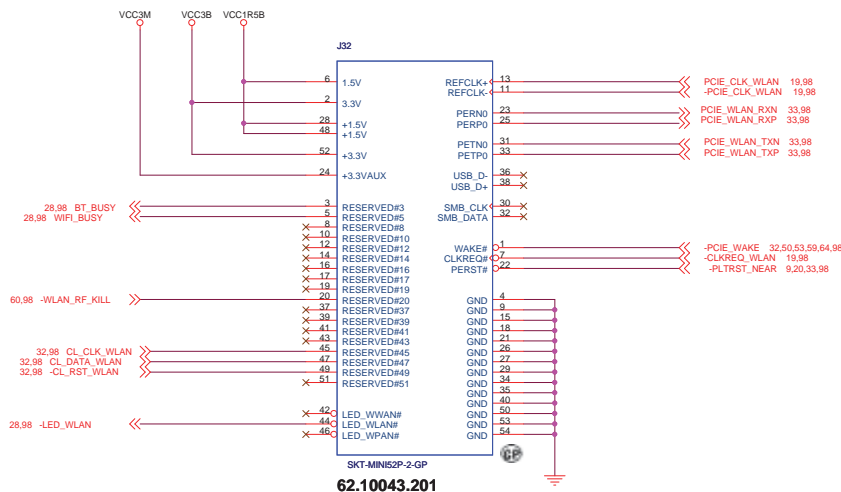


	WWAN MODEL	NON WWAN MODEL
J22	ASM	NO_ASM
J25	ASM	NO_ASM

LOGIC

PLEASE NEAR J32

C7139 PLEASE NEAR J32



<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title				MINI PCIE SLOT			
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TP160 TPAD30 DOCKID0
 TP156 TPAD30 DOCKID1
 TP171 TPAD30 DOCKID2
 TP146 TPAD30 -D_FRES1
 TP154 TPAD30 PCIE_DOCK_RX1P
 TP172 TPAD30 PCIE_DOCK_RX1N
 TP173 TPAD30 -PCIE_WAKE
 TP152 TPAD30 -DOCK_CLKREQ_IN
 TP170 TPAD30 DOCK_LED_CTL0
 TP163 TPAD30 DOCK_LED_CTL1
 TP150 TPAD30 DOCK_SCL
 TP168 TPAD30 DOCK_SDA
 TP167 TPAD30 -PWRRN_DOCK
 TP149 TPAD30 B_ON_DOCK
 TP158 TPAD30 EXT_PWRIS
 TP166 TPAD30 -DASP_DOCK
 TP148 TPAD30 MSCLK
 TP157 TPAD30 MSDATA
 TP165 TPAD30 XKBCLK
 TP147 TPAD30 XKBODATA
 TP165 TPAD30 D_P_SERIRQ
 TP164 TPAD30 ACDC_ID
 TP145 TPAD30 DOCK_CONSUMP
 TP163 TPAD30 -PWRSWITCH
 TP163 TPAD30 SPDIF_OUT
 TP162 TPAD30 PCIE_DOCK_TX1P
 TP151 TPAD30 -PCIE_CLK_DOCK
 TP159 TPAD30 TMSD_SCL
 TP161 TPAD30 TMSD_SDA

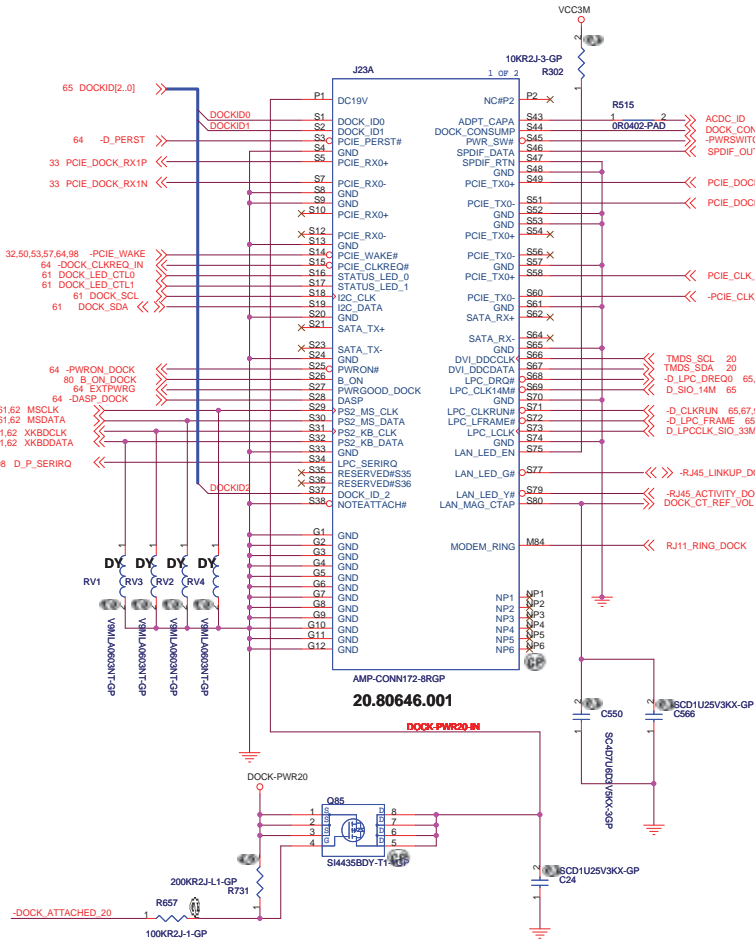
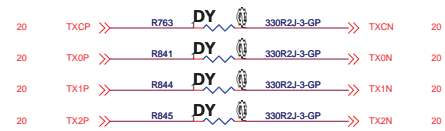
T167 TPAD30 PCIE_DOCK_TX1N
 T185 TPAD30 PCIE_CLK_DOCK
 T165 TPAD30 -D_LPC_DREQ0
 T175 TPAD30 -D_SIO_40T
 T183 TPAD30 -D_CLKRUN
 T163 TPAD30 -D_LPC_FRAME
 T173 TPAD30 -PCIE_WAKE
 T181 TPAD30 -DOCK_CLKREQ_IN
 T161 TPAD30 -D_LPCCLK_SIO_33M
 T171 TPAD30 -RJ45_LINKUP_DOCK
 T179 TPAD30 -RJ45_ACTIVITY_DOCK
 T188 TPAD30 DOCK_CT_REF_VOL
 T178 TPAD30 -DOCK_MICIN_DTCT
 T187 TPAD30 DOCK_HP_OUT_DTCT
 T168 TPAD30 USBP+
 T177 TPAD30 USBP-
 T186 TPAD30 DOCK_EVENT

T174 TPAD30 TXCP
 T162 TPAD30 TXCN
 T162 TPAD30 DOCKCLK_ID3
 T172 TPAD30 DOCKDATA_ID3
 T189 TPAD30 D_LPC_AD0
 T170 TPAD30 D_LPC_AD1

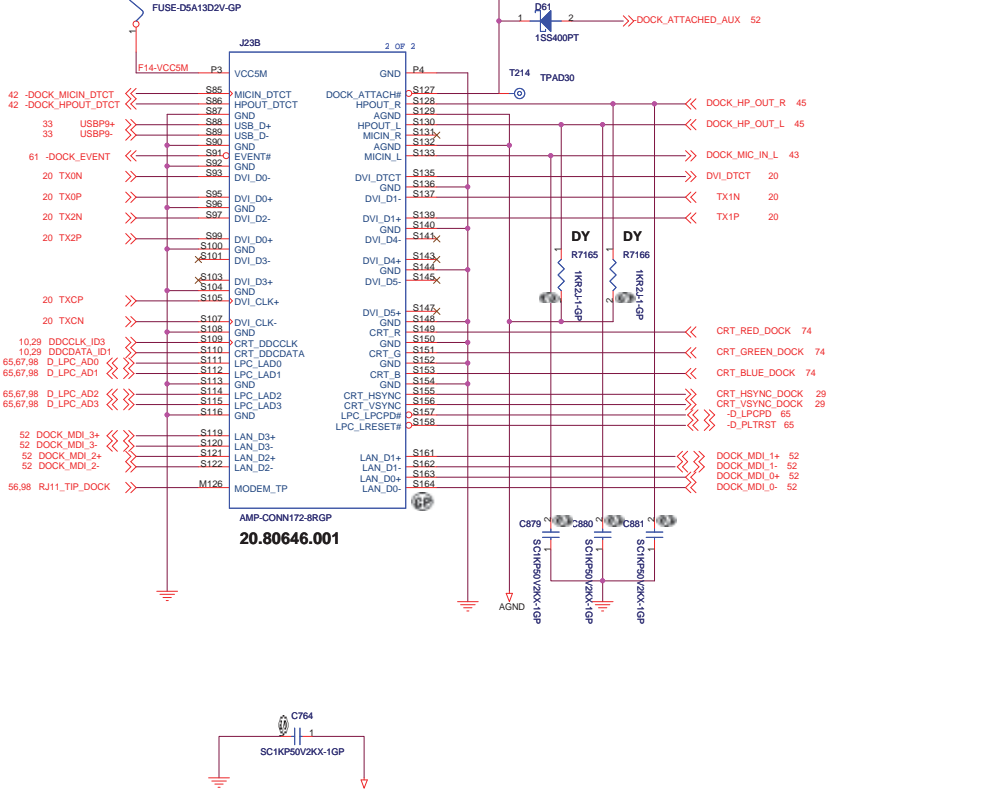
T195 TPAD30 D_LPC_AD2
 T198 TPAD30 D_LPC_AD3
 T191 TPAD30 DOCK_MDI_3+
 T194 TPAD30 DOCK_MDI_3-
 T197 TPAD30 DOCK_MDI_2+
 T190 TPAD30 DOCK_MDI_2-
 T196 TPAD30 DOCK_HP_OUT_R
 T199 TPAD30 DOCK_HP_OUT_L
 T192 TPAD30 DOCK_MIC_IN_L
 T206 TPAD30 DVI_DTCT

T200 TPAD30 TX1P
 T204 TPAD30 CRT_RED_DOCK
 T207 TPAD30 CRT_GREEN_DOCK
 T201 TPAD30 CRT_BLUE_DOCK
 T202 TPAD30 CRT_HSYNC_DOCK
 T205 TPAD30 CRT_VSYNC_DOCK
 T209 TPAD30 -D_LPCPD
 T203 TPAD30 -D_PLTRST
 T210 TPAD30 DOCK_MDI_1+
 T212 TPAD30 DOCK_MDI_1-
 T213 TPAD30 DOCK_MDI_0+
 T211 TPAD30 DOCK_MDI_0-

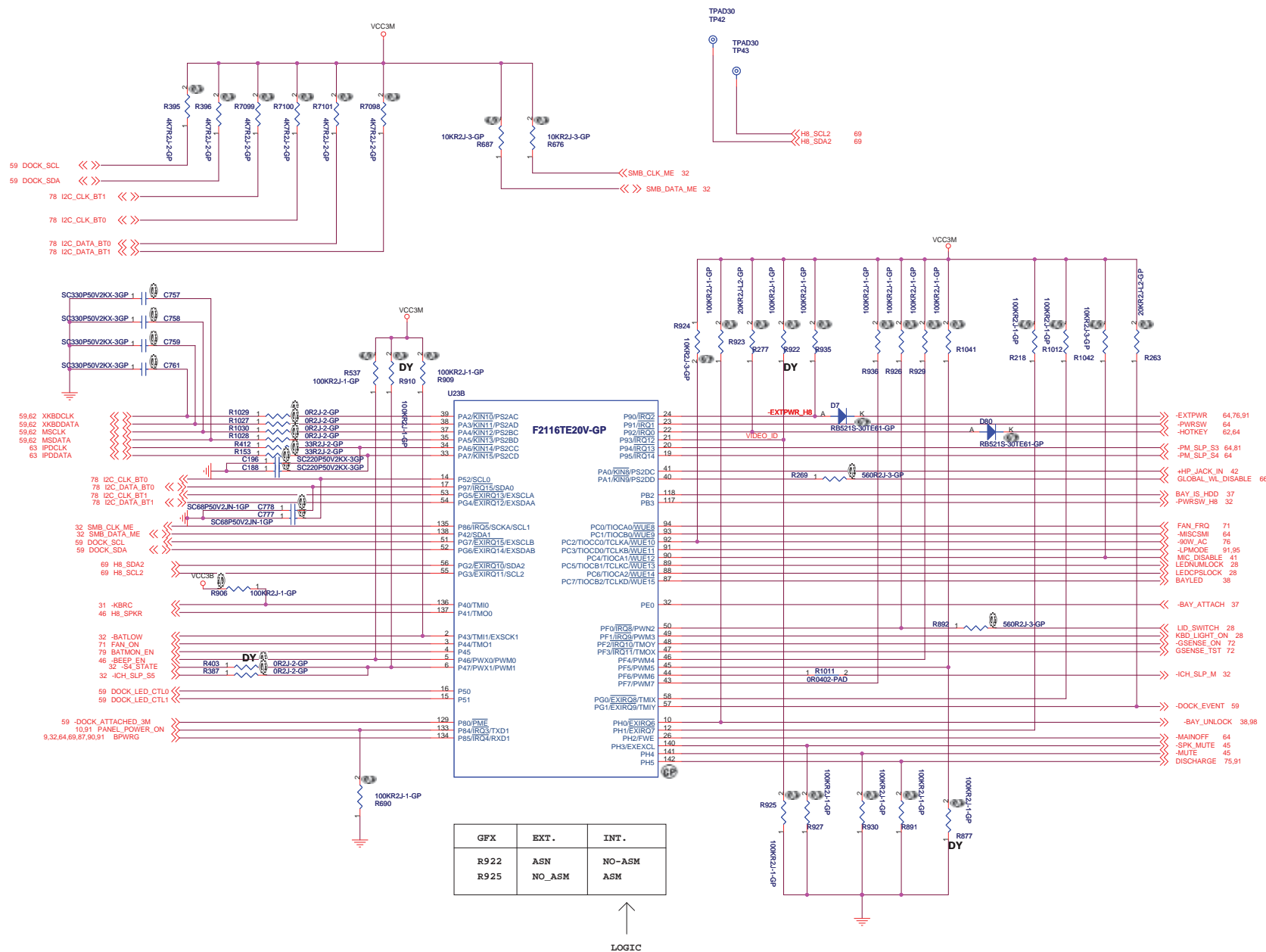
CLOSE TO DOCK CONN



Polysw 0.75A,
 0.29 ohm 0.4W



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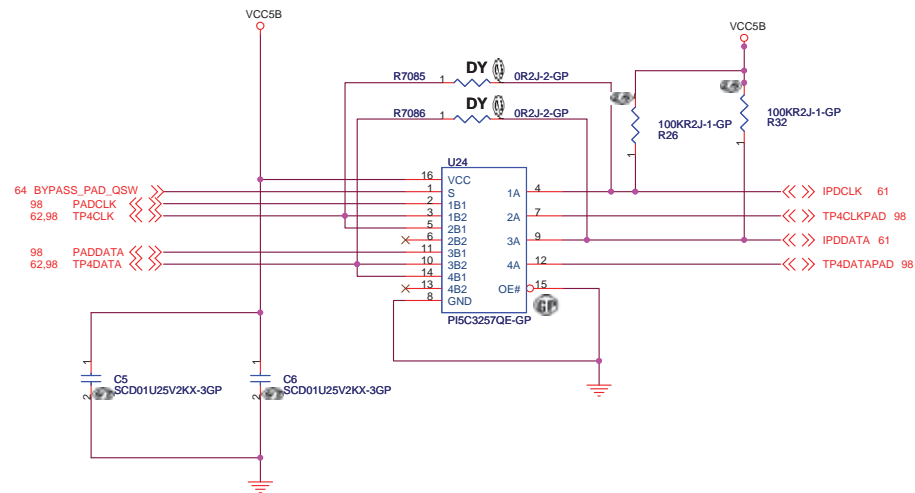
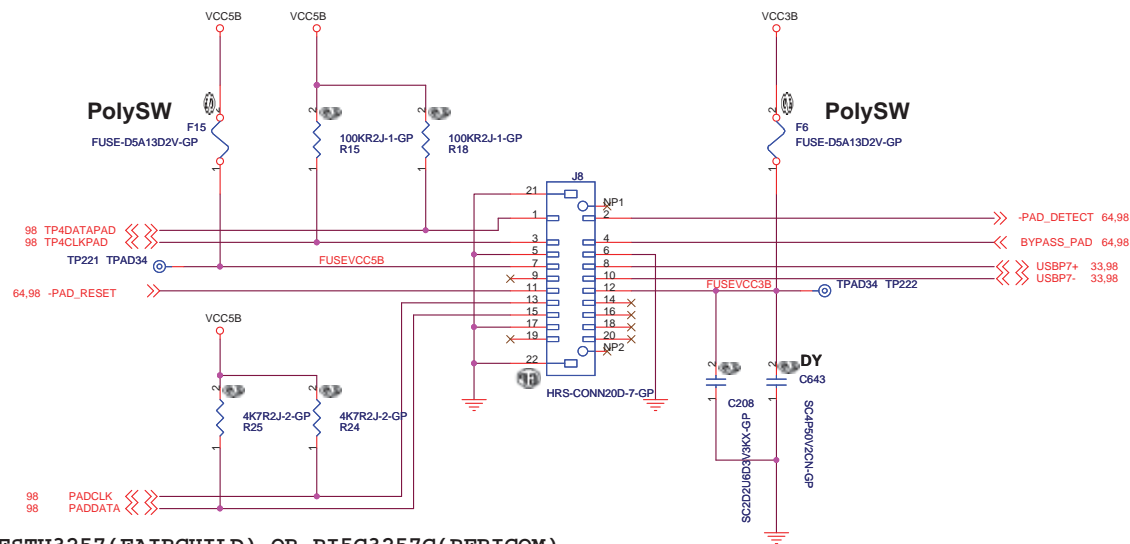


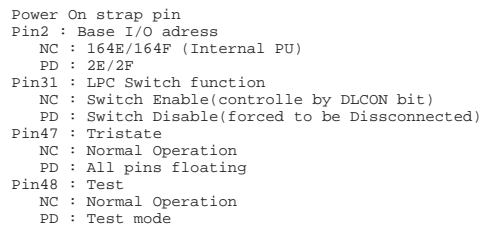
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

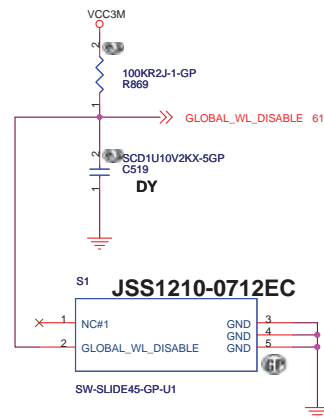
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Size	Document Number	Rev	1
Custom	Rnote-4		
Date: Friday, April 20, 2007	Sheet 61	of	99





```
GPIO00 :LPC RESET# FOR DOCKING LPC DEVICE
GPIO01 :LPC POWER DOWN FOR DOCKING LPC DEVICE
GPIO02 :LPC RESET# FOR LEGACY IO BAY
GPIO03 :LPC POWER DOWN FOR LEGACY IO BAY
GPIO04 :DOCK ID 0
GPIO20 :DOCK ID 1
GPIO21 :DOCK ID 2
GPIO23 :RESERVED(INTERNAL PULLUP ENABLE)
```

WIRELESS DISABLE SW

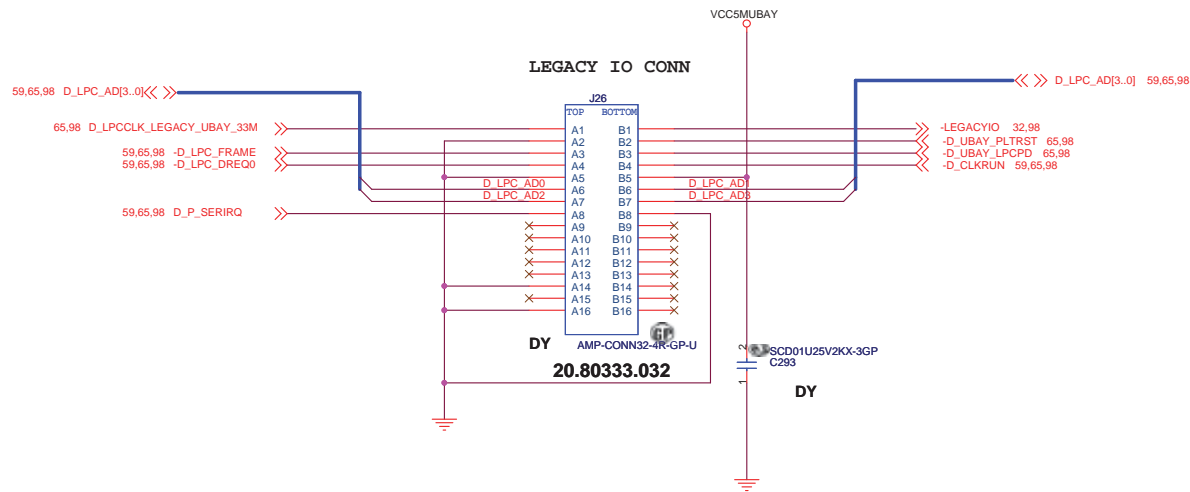


**MUST APPLY
S1 PART
NUMBRE**

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Title		WIRELESS DISABLE SW	
Size	Document Number	Rev	
Custom		Rnote-4	
Date:	Friday, April 20, 2007	Sheet	66 of 99




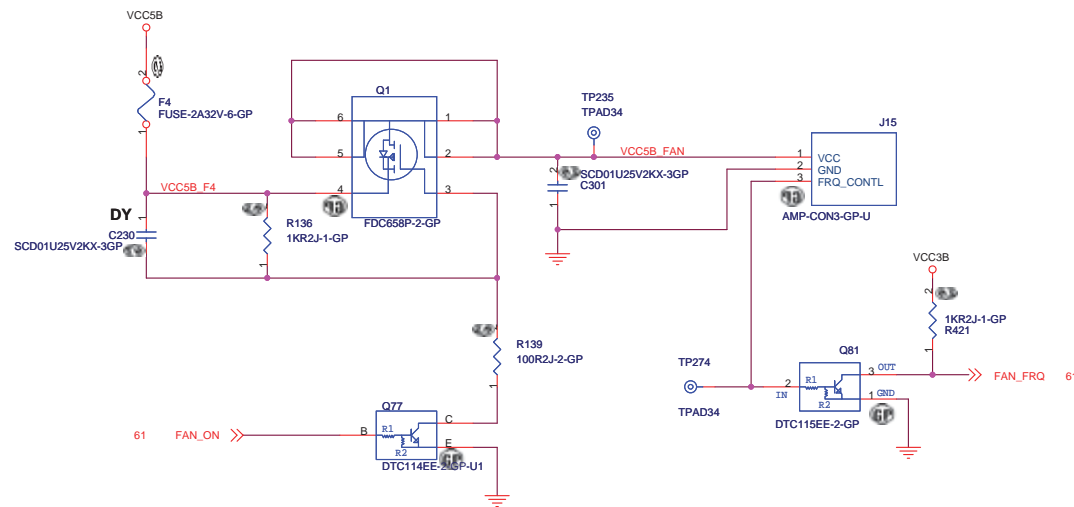
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Title			
LEGACY I/O CONNECTOR			
Size	Document Number		Rev
Custom			1
Date:	Friday, April 20, 2007	Sheet	67 of 99

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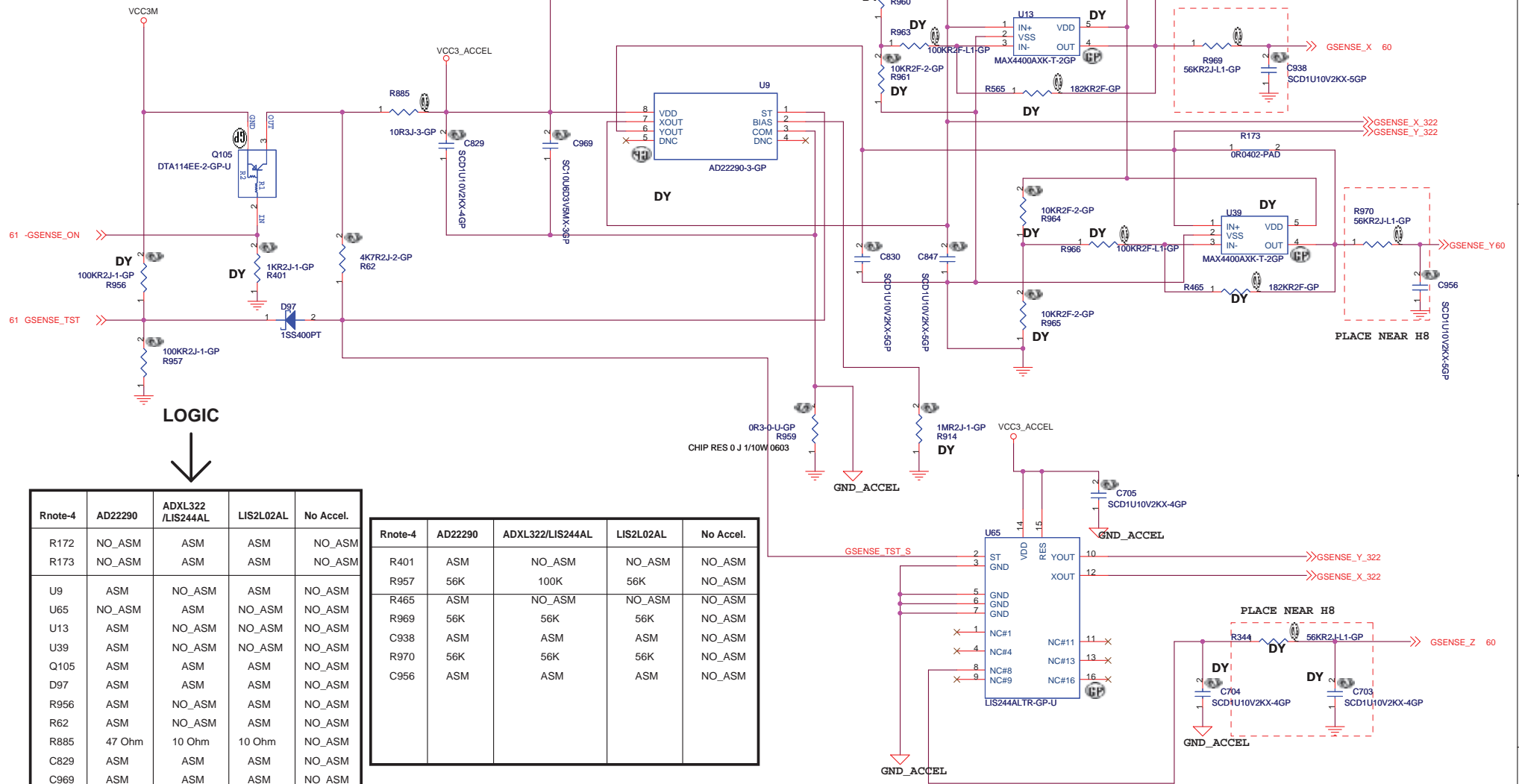
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Title		
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Size	Document Number	Rev
Custom	Rnote-4	1
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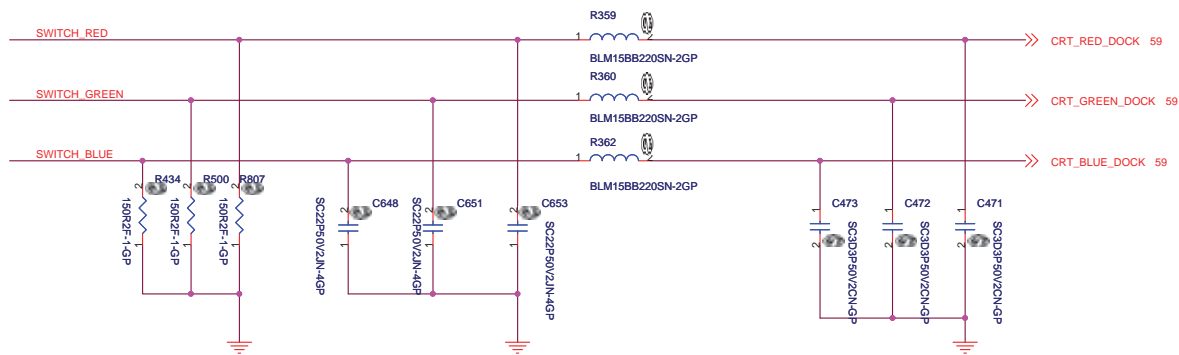
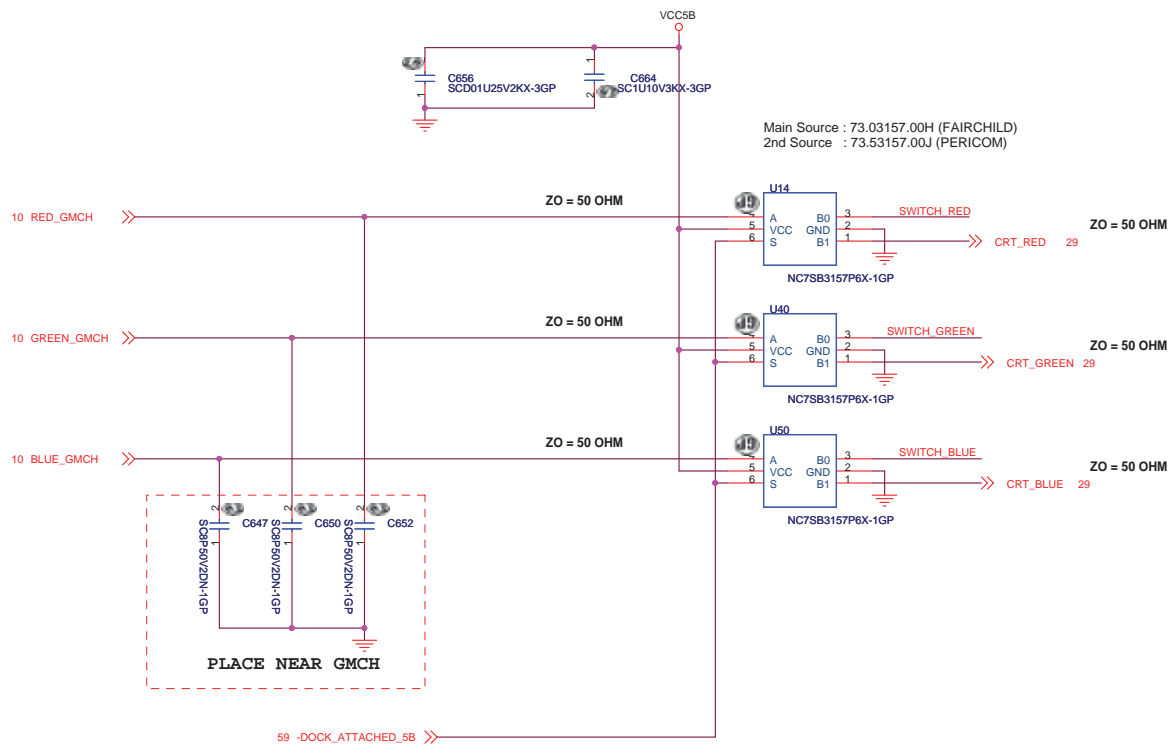
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Title	
FAN CONTROL	
Size	Document Number
Custom	Rnote-4
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Rev	1



SIV(DV2) : return to add all the part on page-73 and define placement

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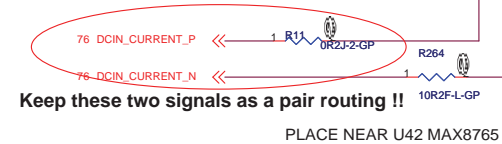
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Title		
EMC DECOUPLING		
Size	Document Number	Rev
Custom	Rnote-4	1
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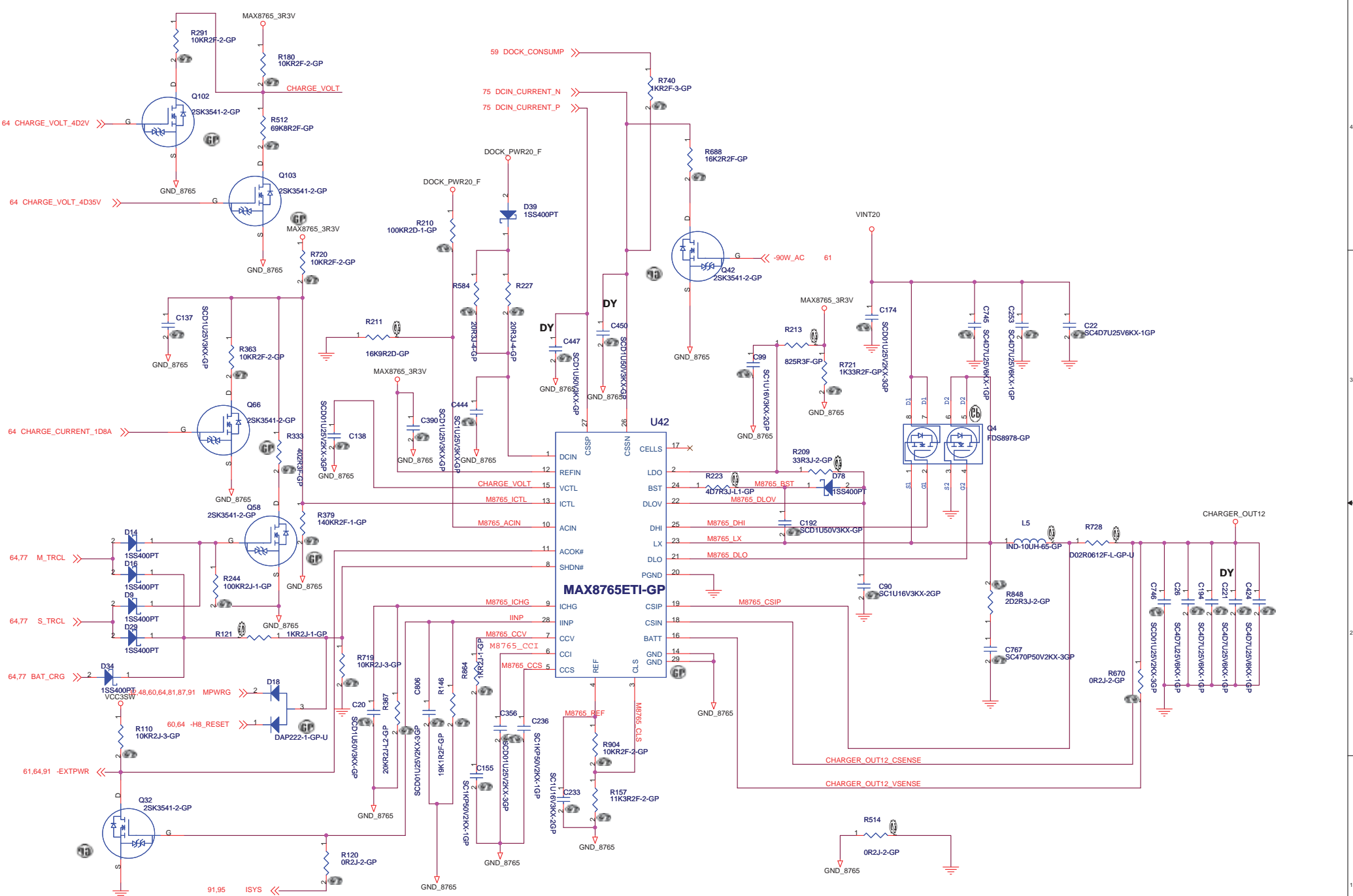
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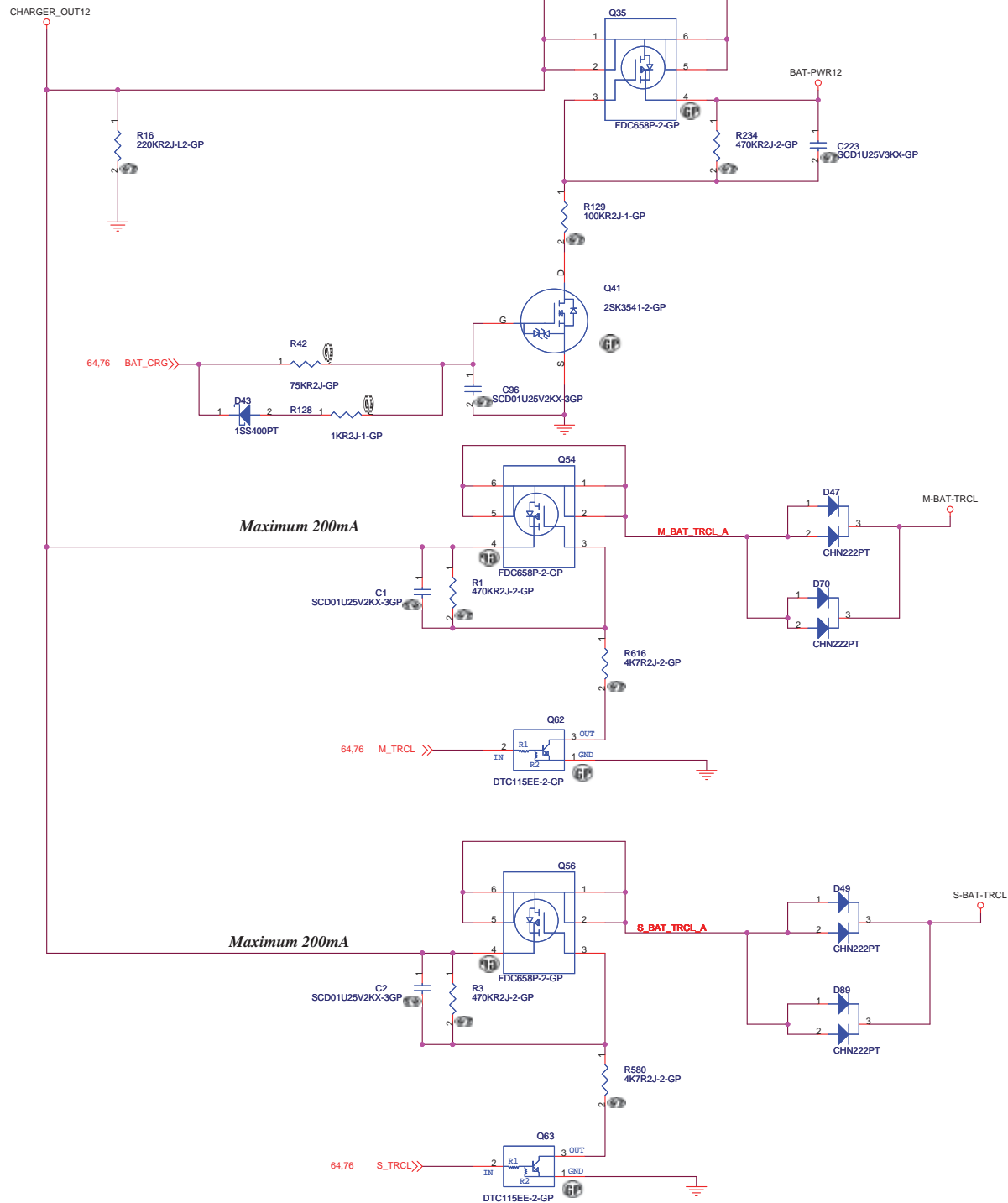
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Title			
RGB SWITCH			
Size Custom	Document Number Rnote-4		Rev 1
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PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

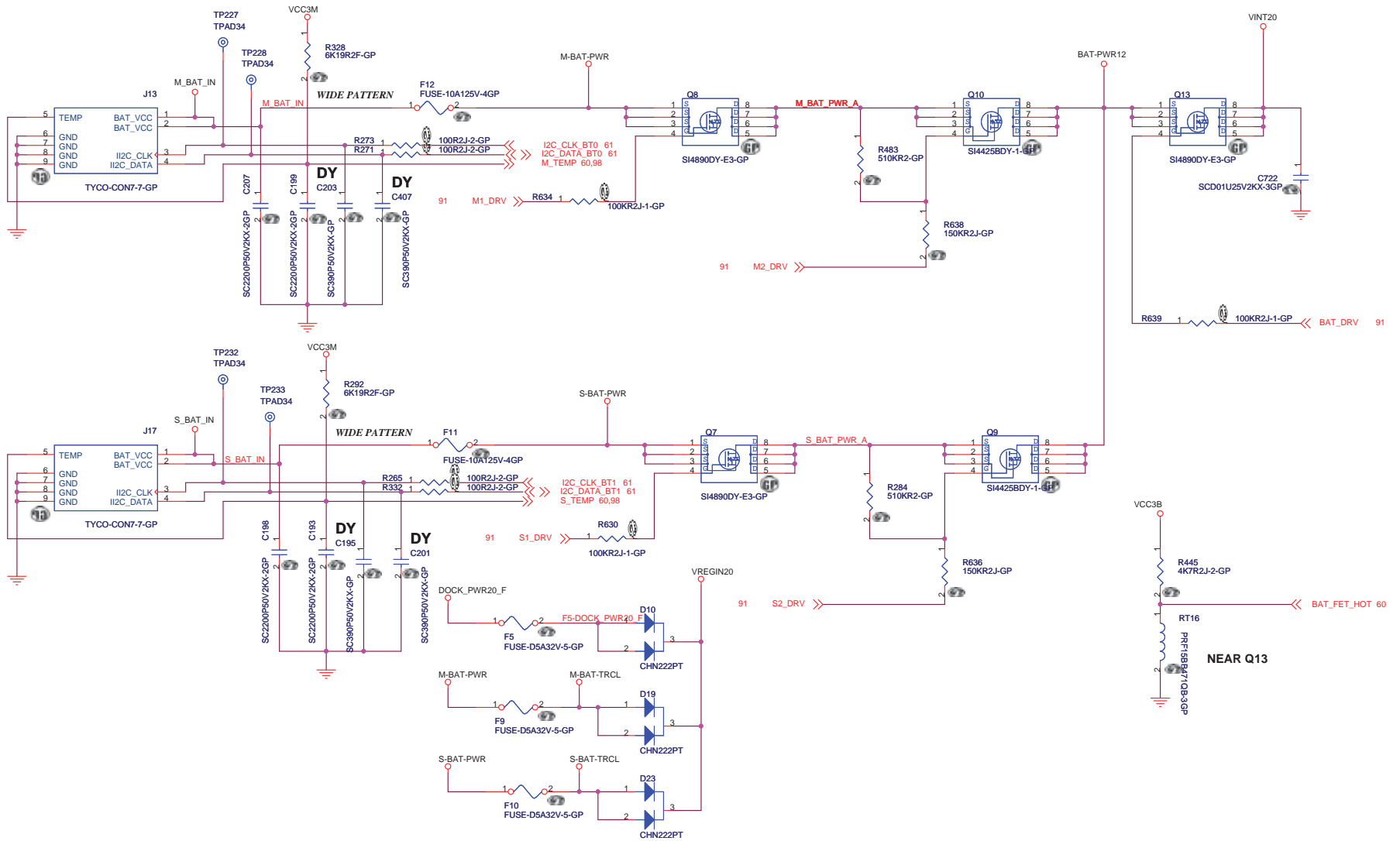


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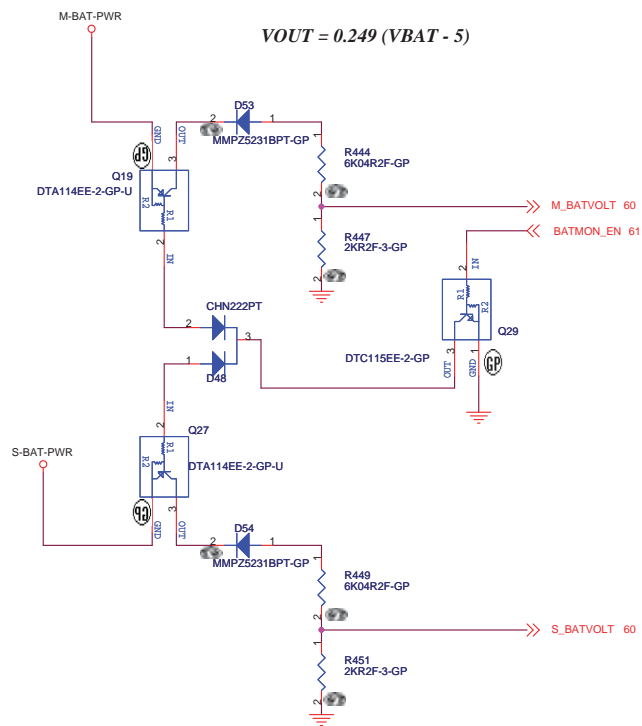


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Size	Document Number	Rnote-4	
Custom		Rev 1	
Date: Friday, April 20, 2007	Sheet 77	of 99	

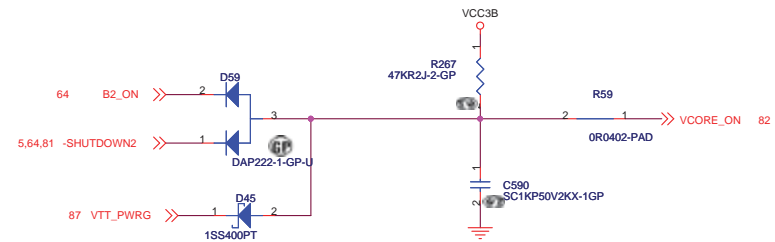
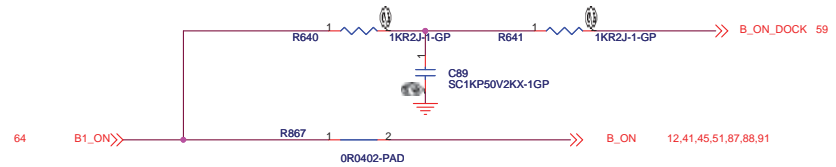
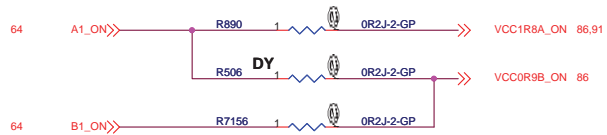
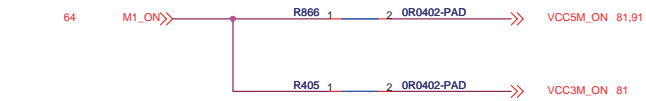


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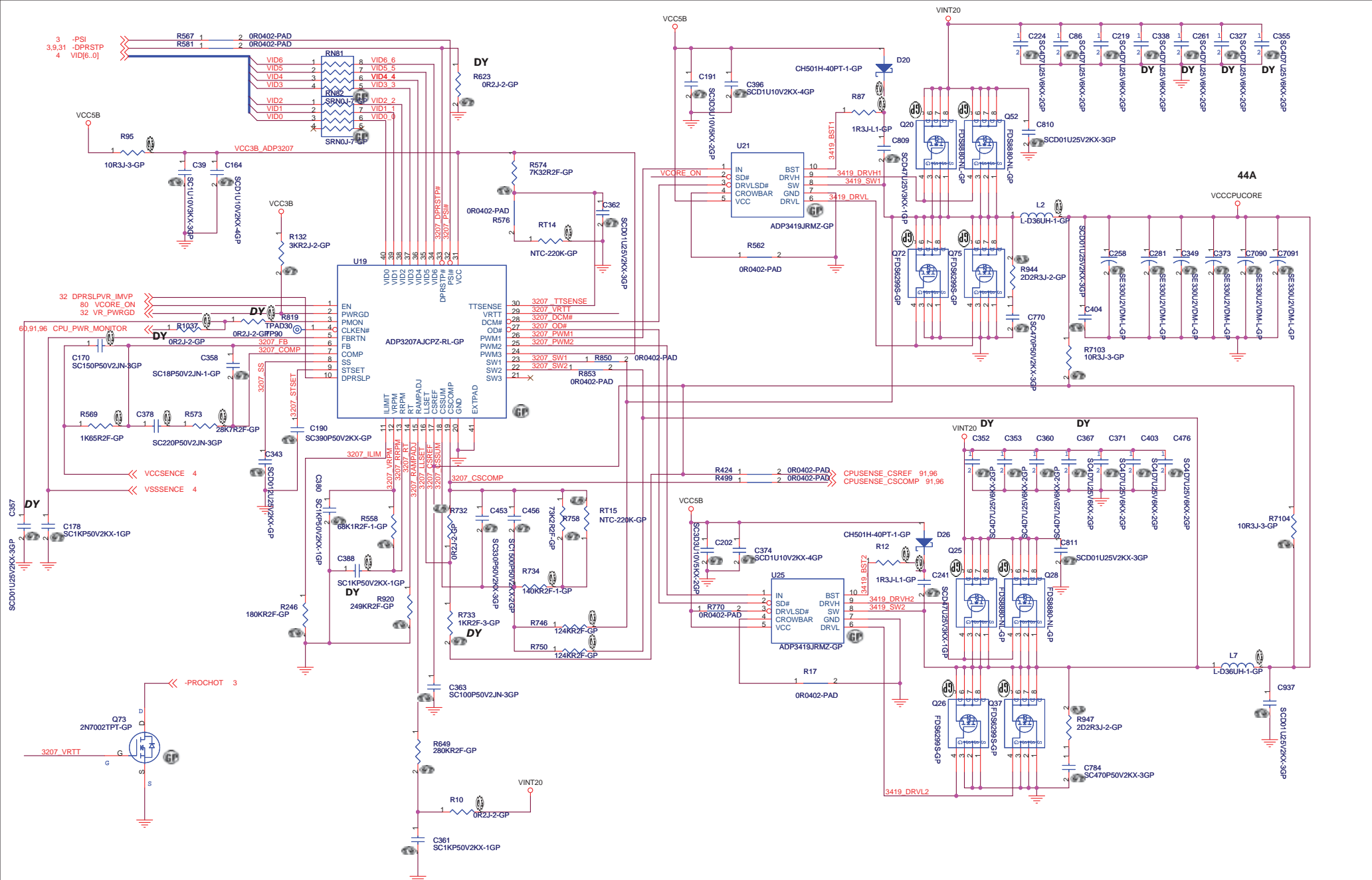
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Title			
BATTERY MONITOR			
Size	Document Number		Rev
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<Variant Name>			
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Title			
POWER SEQUENCE			
Size	Document Number	Rev	
Custom		1	
Date:	Friday, April 20, 2007	Sheet	80 of 99

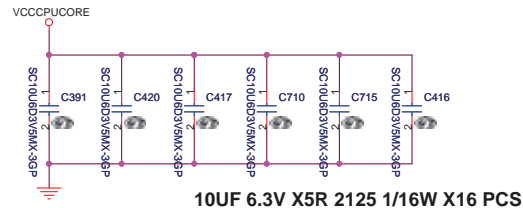
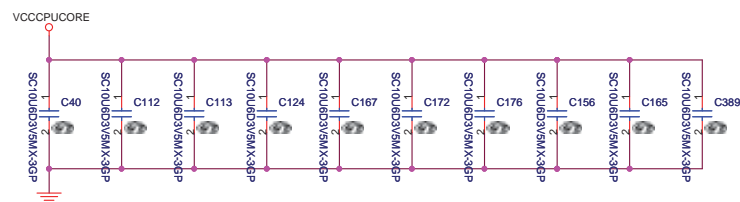
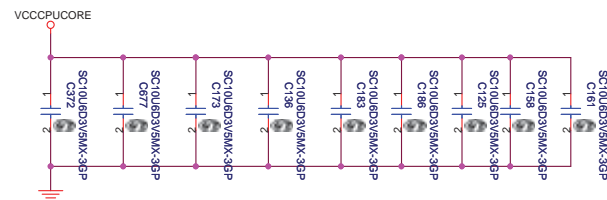
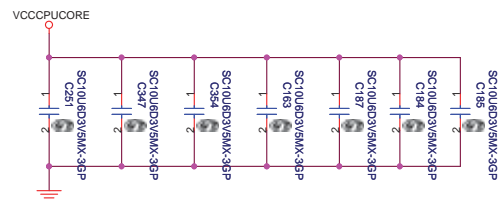


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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

DC-DC VCCCPUCORE			Rev
Title	Document Number	Custan	1
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緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title

Size

Custom

Document Number

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Rev

1

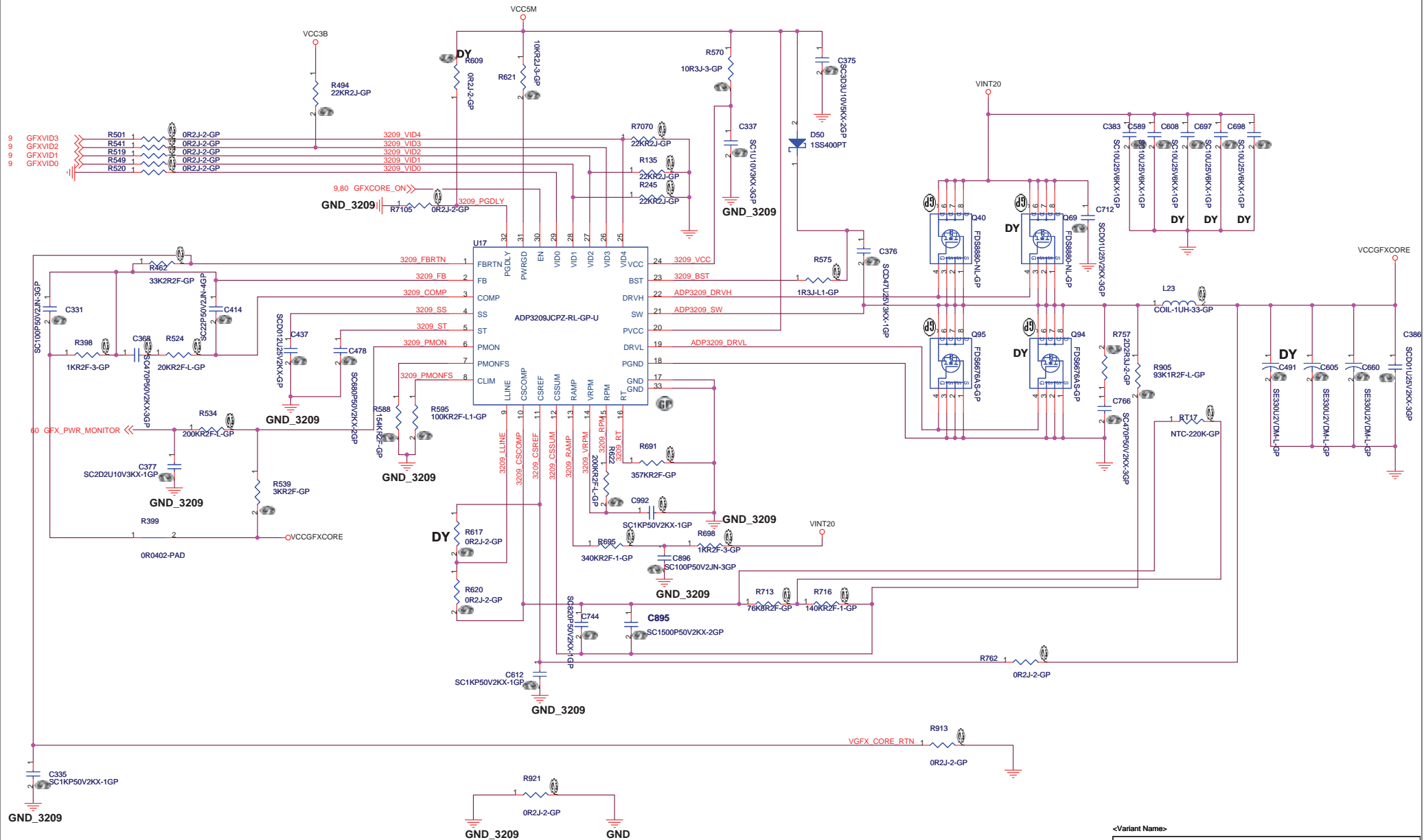
Date: Friday, April 20, 2007

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Rnote-4

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VID4=0
VID3=1
VID2=0
VID1=0
VID0=0

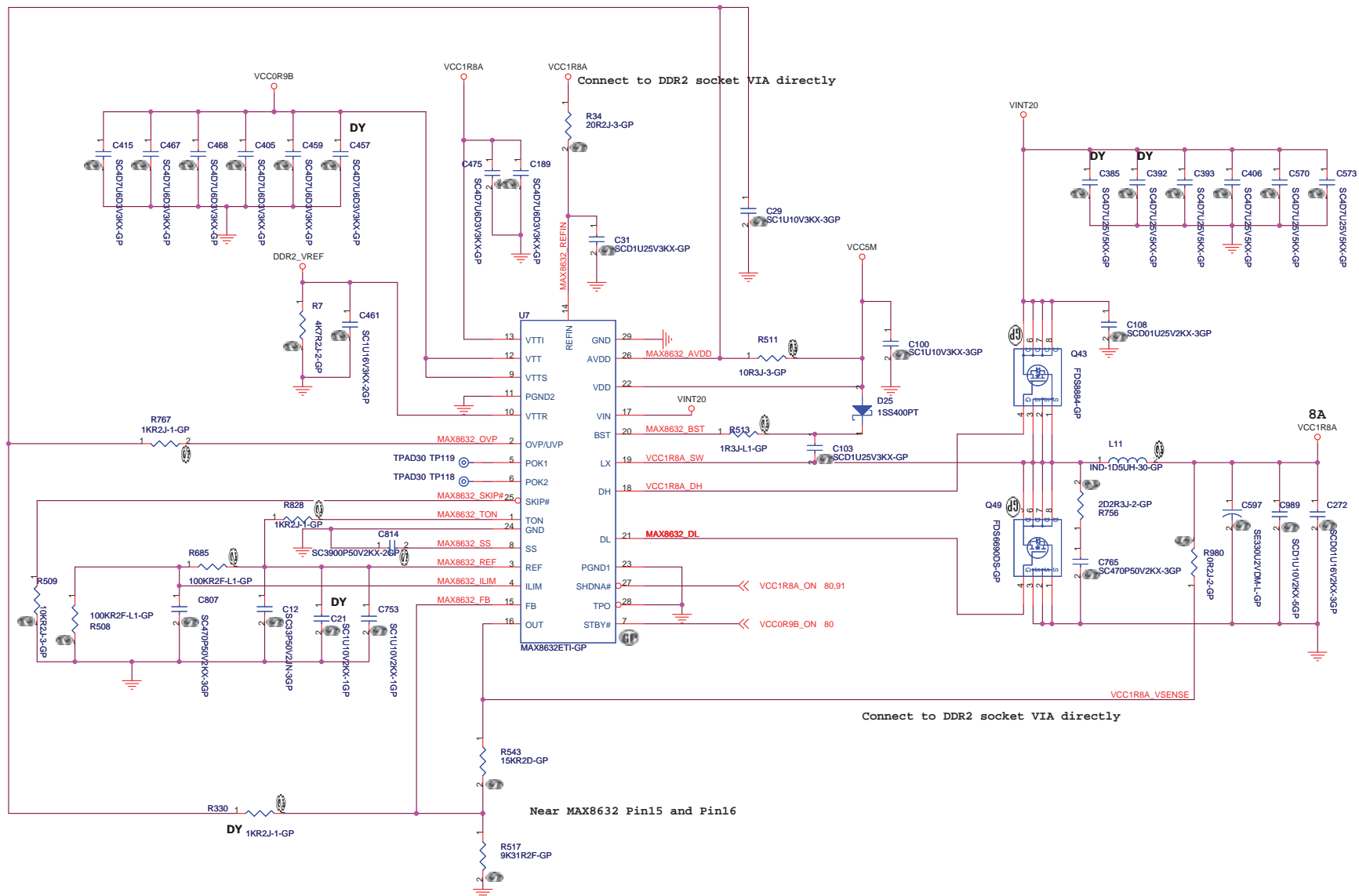
INTEG VCCGFXCORE VALUE
1.NORMAL; 1.05V
2.GRAPHICS RENDER STANDBY MODE; 0.75V
VID<4..0>
0100=1.05V



<http://hobi-elektronika.net>

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Title DC-DC GFX CORE		
Size Custom	Document Number Rnote-4	Rev 1
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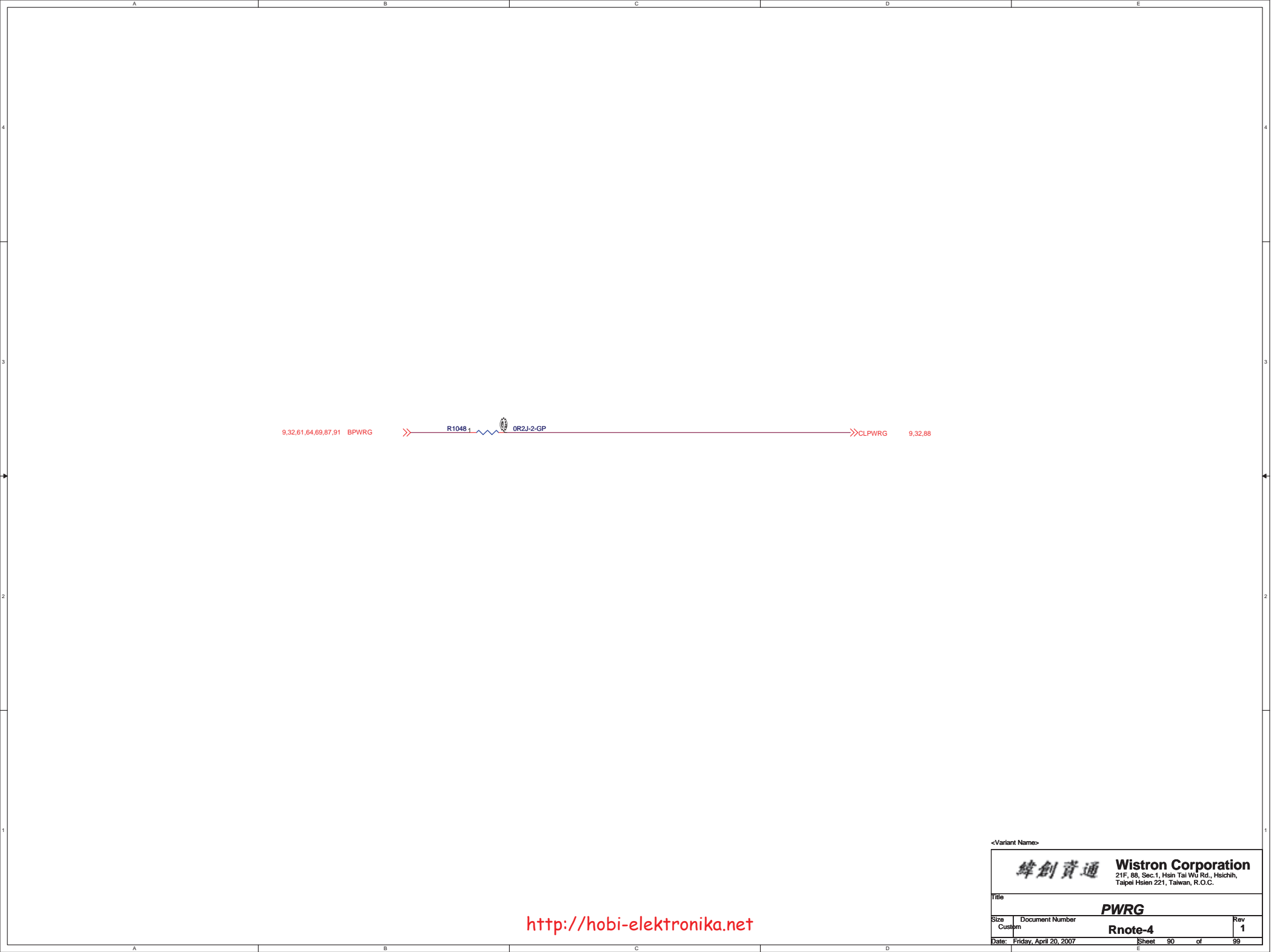
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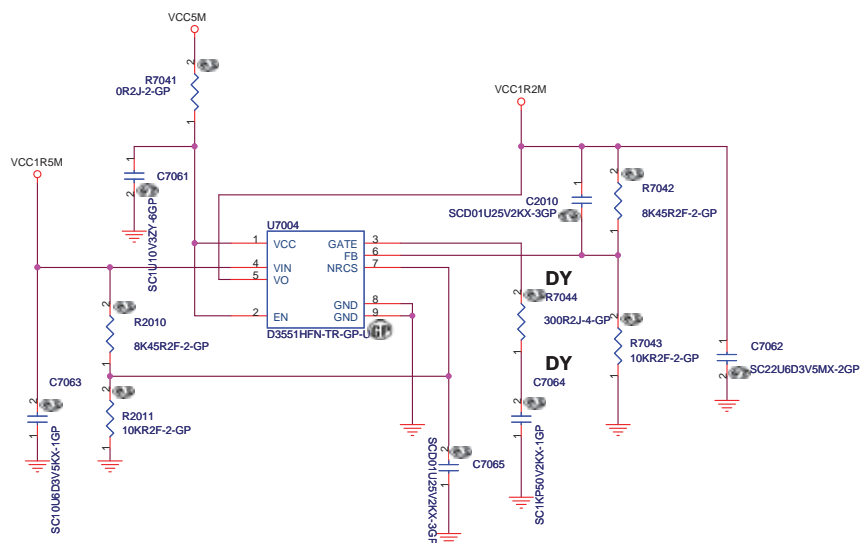
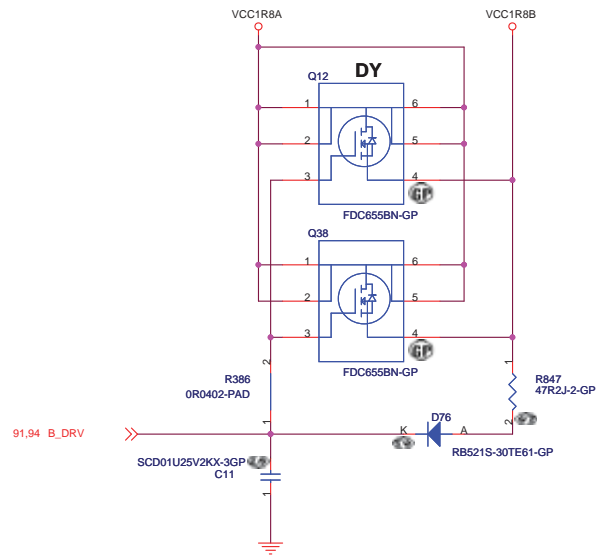
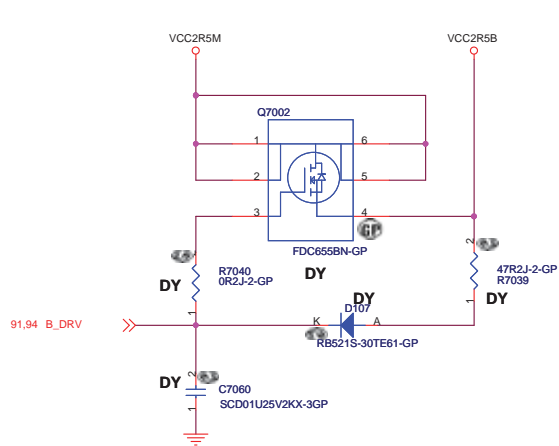


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Size	Document Number		Rev
Custom		Rnote-4	1
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Custom	Rnote-4	
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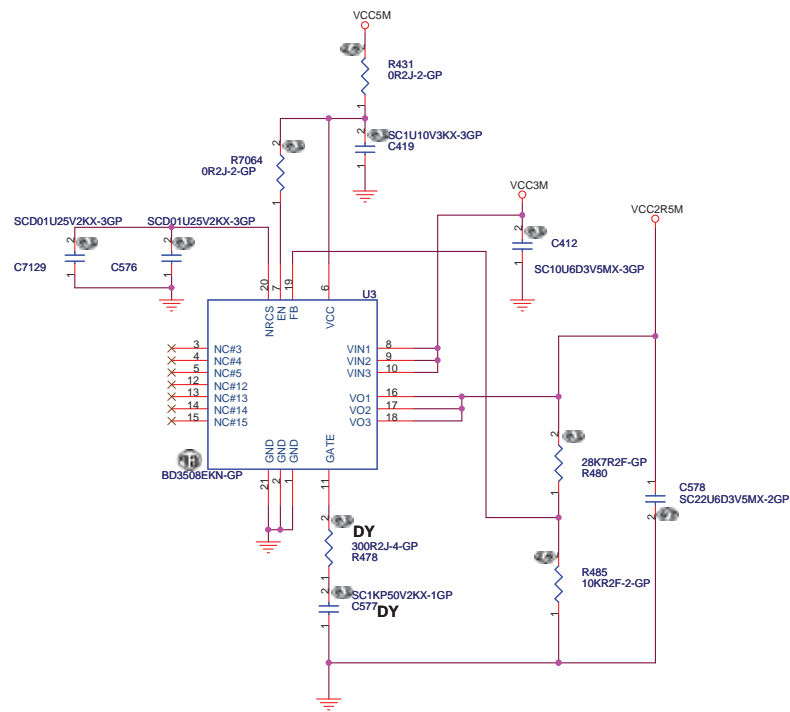




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Title		LOAD SW (B&M)	
Size	Document Number	Rev	
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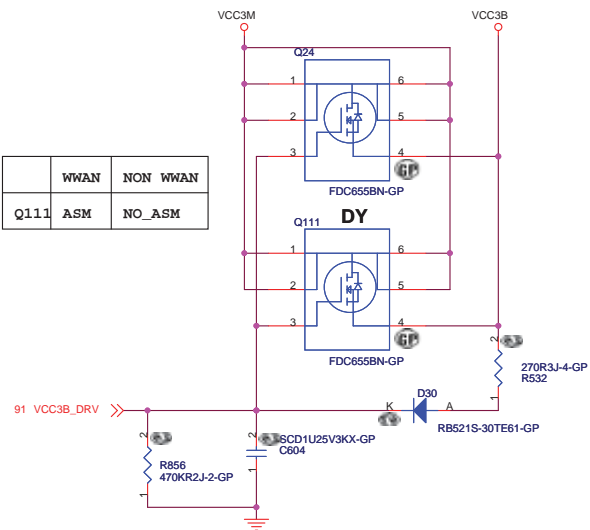


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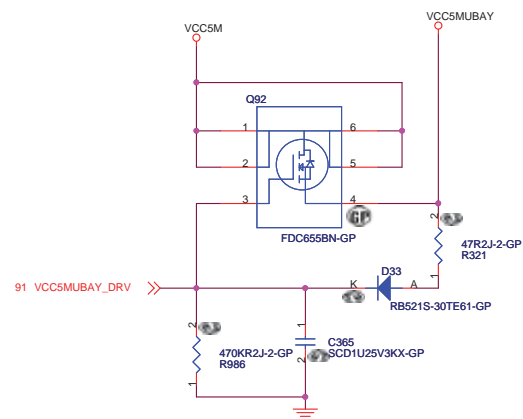
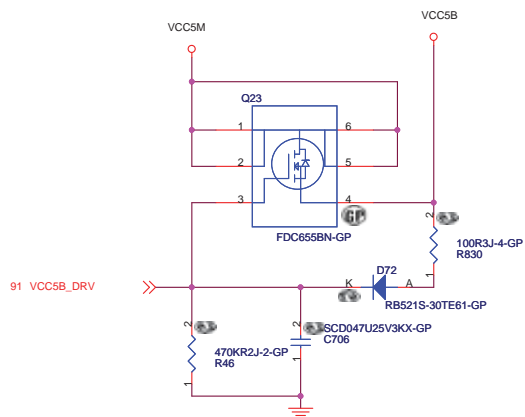
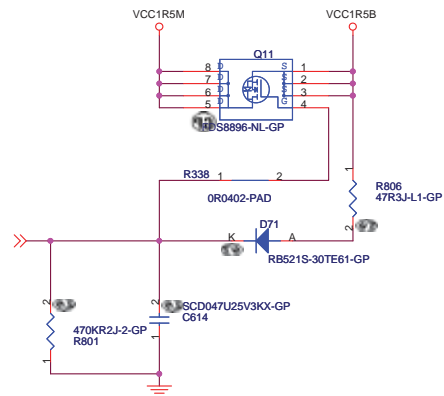
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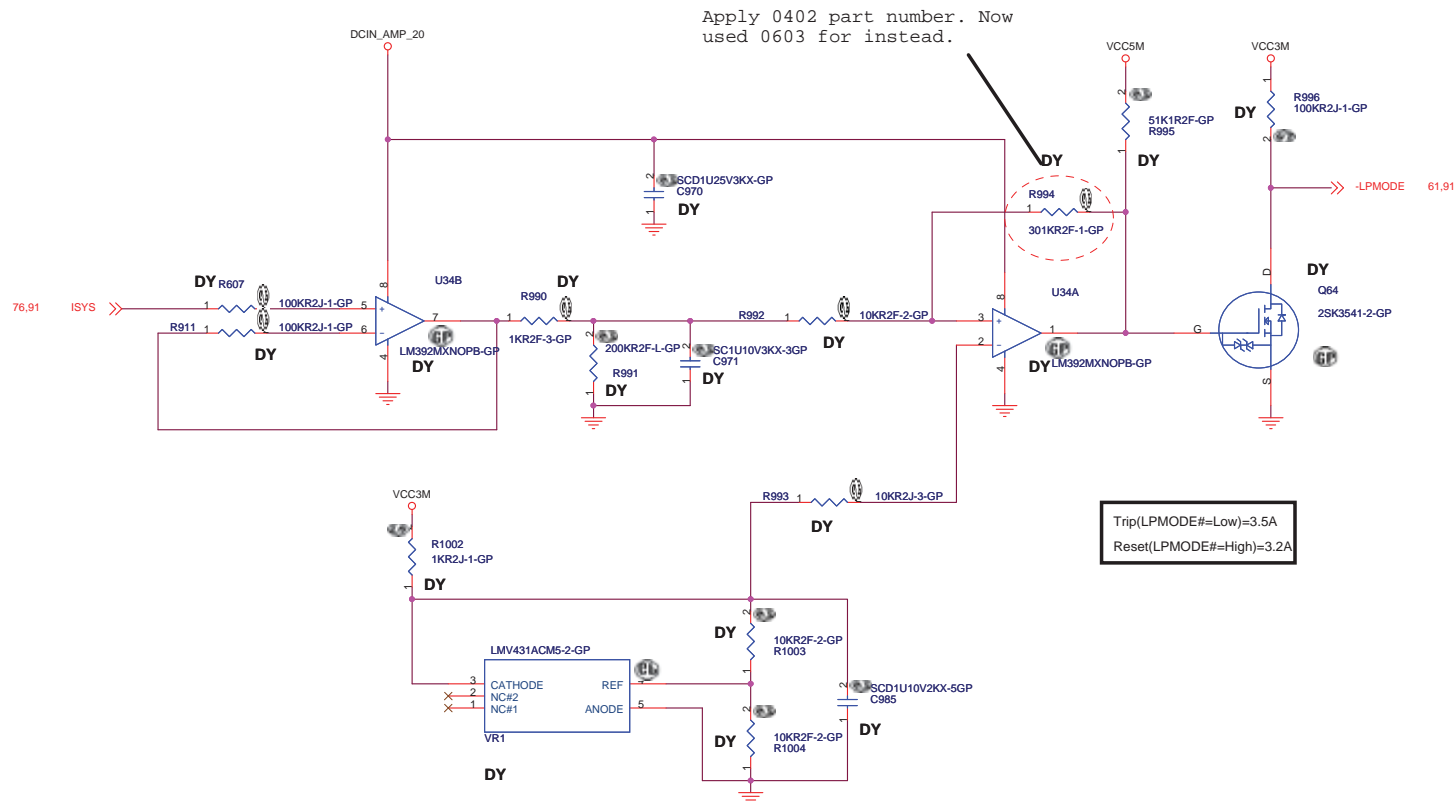
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Title			
LOAD SW (VCC2R5M)			
Size	Document Number	Rev	
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	WWAN	NON_WWAN
Q111	ASM	NO_ASM

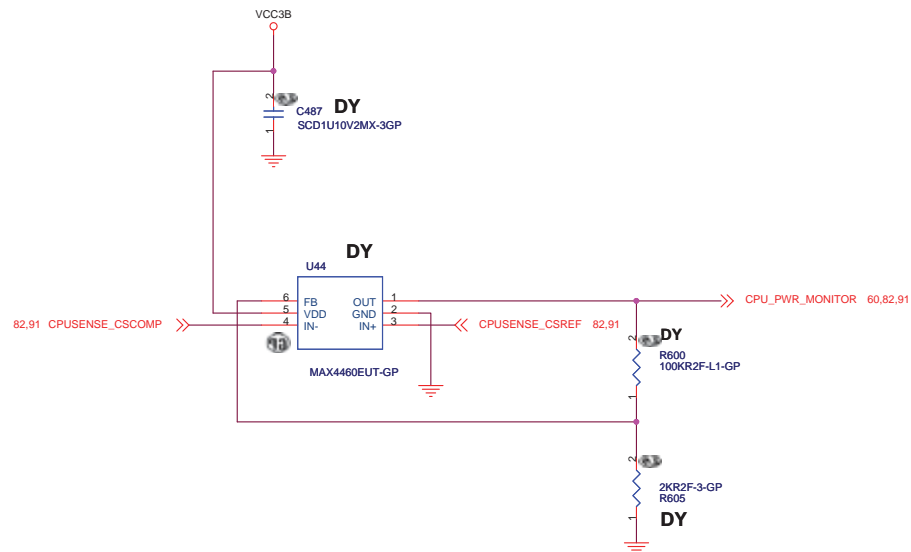


91,92 B_DRV




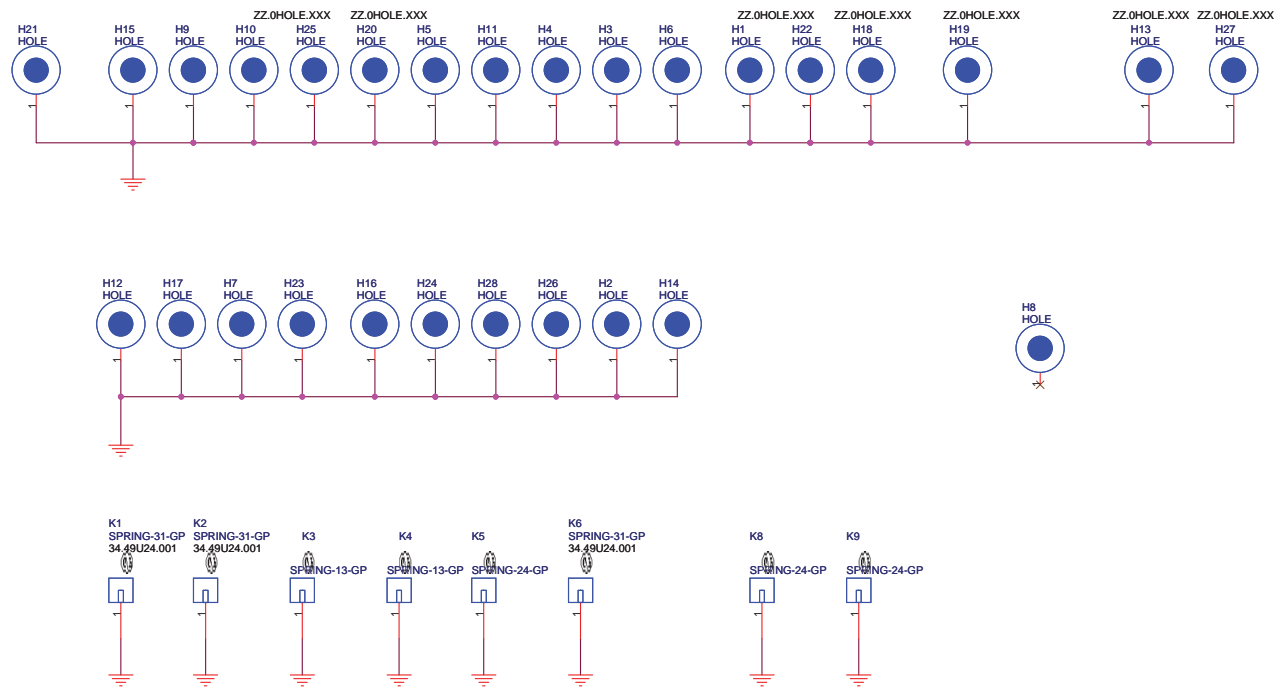


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Title			
CPU POWER MONITOR			
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<Variant Name>			
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PTH FOR SCREW HOLES			
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Size		1	
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Rockwell-4 SDV

1. TITLE PAGE
2. REFERENCE
3. CPU(1/3)
4. CPU(2/3)
5. CPU(3/3)
6. ITP CON/N
7. CRESTLINE(1/7) : HOST I/F
8. CRESTLINE(2/7) : DDR2
9. CRESTLINE(3/7) : DMI/PM/CFG/GF
10. CRESTLINE(4/7) : GRAPHICS
11. CRESTLINE(5/7) : VCC1
12. CRESTLINE(6/7) : VCC2
13. CRESTLINE(7/7) : GND
14. <BLANK PAGE>
15. DDR2 SODIMM-A
16. DDR2 SODIMM-B
17. DDR2 TERMINATION
18. DDR2 DECOUPLING
19. CLOCK GEN
20. DVI TRANSMITTER
21. <BLANK PAGE>
22. <BLANK PAGE>
23. <BLANK PAGE>
24. <BLANK PAGE>
25. <BLANK PAGE>
26. <BLANK PAGE>
27. <BLANK PAGE>
28. LCD CONNECTOR
29. EXT CRT INTERFACE
30. PATA I/F PU
31. ICH8-M(1/4):ATA/AC97/LPC
32. ICH8-M(2/4) : PM/SMB/GPIO

33. ICH8-M(3/4) : PCI/PCIE/DMI/USB/SPI
34. ICH8-M(4/4) : VCC/GND
35. RTC BATTERY
36. SATA HDD CONN
37. BAY I/F CONN
38. USB POWER/CONNECTOR
39. AUDIO AD1984JCP
40. AUDIO MISC
41. AUDIO INT MIC
42. AUDIO HP & SENSE
43. AUDIO EXT / DOCK MIC AMP
44. AUDIO SYSTEM SPEAKER
45. AUDIO HDP AMP
46. AUDIO HDP AMP MISC
47. MDC 1.5 CONNECTOR
48. CARDBUS CONTROLLER(1/2)
49. CARDBUS CONTROLLER(2/2)
50. CARDBUS/EXC SLOT
51. CARDBUS/EXC POWER CONTROL
52. GBE LAN SW
53. BCM5787M
54. <BLANK PAGE>
55. GBE MAGNETICS
56. RJ11/RJ45 CONNECTOR
57. MINI PCIE SLOT
58. SPI FLASH
59. DOCKING CONNECTOR
60. H8S/2116B (1/2)
61. H8S/2116B (2/2)
62. KEYBOARD CONN
63. TOUCH PAD CONNECTOR
64. MISC G/A (PMH7)

65. SUPER I/O (WPCN385)
66. WIRELESS DISABLE SW
67. LEGACY IO CONNECTOR
68. <BLANK PAGE>
69. THERMAL SENSOR
70. TCPA /TATER
71. FAN CONTROL
72. G-SENSOR
73. EMC DECOUPLING
74. RGB SWITCH
75. DC-IN
76. BATTERY CHARGER (MAX8765)
77. CHARGE SELECT
78. BATTERY INPUT
79. BATTERY MONITOR
80. POWER SEQUENCE
81. DC-DC VCC3M/VCC5M (MAX17003)
82. DC-DC VCCCPUCORE (ADP3207A)
83. VCCCPUCORE DECOUPLING
84. <BLANK PAGE>
85. DC-DC VCCGFXCORE(ADP3209)
86. DC-DC VCC1R8A/VCC0R9B (MAX8632)
87. DC-DC VCC1R05B/VCC1R5M (MAX1540)
88. DC-DC VCC1R25B (APL5912)
89. <BLANK PAGE>
90. PWRG
91. RINKAN-1
92. LOAD SW (VCC2R5B/VCC1R8B/VCC1R2M)
93. LOAD SW (VCC2R5M)
94. LOAD SW (VCC3B/VCC5B/VCC5MUBAY/VCC1R5B)
95. LPMODE
96. CPU POWER MONITOR

97. PTH FOR SCREW HOLES
98. All I/O Test point
99. Sheet Index
100. History

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<Variant Name>			
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
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Date	EC Number
10/30/06	EC-SDV_EC001
11/15/06	R4-SIV_EC003
11/15/06	R4-SIV_EC004
11/15/06	R4-SIV_EC005
11/15/06	R4-SIV_EC006
11/15/06	R4-SIV_EC007
11/15/06	R4-SIV_EC008
11/15/06	R4-SIV_EC009
11/15/06	R4-SIV_EC010
11/15/06	R4-SIV_EC011
11/15/06	R4-SIV_EC012
11/15/06	R4-SIV_EC013
11/15/06	R4-SIV_EC014
11/15/06	R4-SIV_EC015
11/15/06	R4-SIV_EC016
11/15/06	R4-SIV_EC017
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